

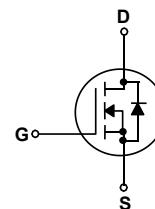
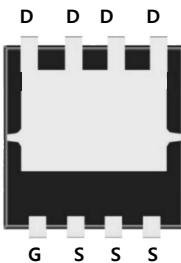
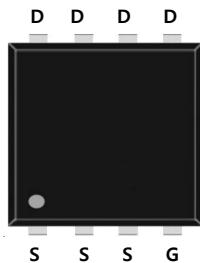
Features

- Enhancement mode
- Fast switching and high efficiency
- 100% avalanche tested

Product Summary



V_{DS}	40	V
$R_{DS(on),Typ} @ V_{GS}=10\text{ V}$	5.8	$\text{m}\Omega$
I_D	40	A



PDFN3.3*3.3-8

Maximum ratings, at $T_A = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
$V(BR)DSS$	Drain-Source breakdown voltage	40	V	
V_{GS}	Gate-Source voltage	± 20	V	
I_S	Diode continuous forward current	40	A	
I_D	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C=25^\circ\text{C}$	40	A
		$T_C=100^\circ\text{C}$	34	A
I_{DM}	Pulse drain current tested ①	$T_C=25^\circ\text{C}$	160	A
E_{AS}	Avalanche energy, single pulsed ②	20	mJ	
P_D	Maximum power dissipation	$T_C=25^\circ\text{C}$	30	W
		$T_C=100^\circ\text{C}$	12	W
$T_{STG,TJ}$	Storage and Junction Temperature Range	-55 to 150	$^\circ\text{C}$	

Thermal Characteristics

Symbol	Parameter	Max	Unit
R_{eJC}	Thermal Resistance, Junction-to-Case	3.4	$^\circ\text{C/W}$
R_{eJA}	Thermal Resistance, Junction-to-Ambient	75	$^\circ\text{C/W}$

Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_j = 125^\circ\text{C}$)	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.6	2.5	V
$R_{\text{DS}(\text{on})}$	Drain-Source On-State Resistance ④	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$	--	5.8	8.0	$\text{m}\Omega$
		$T_j = 100^\circ\text{C}$	--	7.8	--	$\text{m}\Omega$
$R_{\text{DS}(\text{on})}$	Drain-Source On-State Resistance ④	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=10\text{A}$	--	8.7	11	$\text{m}\Omega$

Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)

C_{iss}	Input Capacitance	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	773	--	pF
C_{oss}	Output Capacitance		--	304	--	pF
C_{rss}	Reverse Transfer Capacitance		--	25	--	pF
R_g	Gate Resistance	$f=1\text{MHz}$	0.2	1.7	5	Ω
$Q_g(10\text{V})$	Total Gate Charge	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=20\text{A}, V_{\text{GS}}=10\text{V}$	--	15	--	nC
$Q_g(4.5\text{V})$	Total Gate Charge		--	7.7	--	nC
Q_{gs}	Gate-Source Charge		--	3	--	nC
Q_{gd}	Gate-Drain Charge		--	3.4	--	nC

Switching Characteristics

$T_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=20\text{V}, I_{\text{D}}=20\text{A}, R_{\text{G}}=3\Omega, V_{\text{GS}}=10\text{V}$	--	5.6	--	ns
T_{r}	Turn-on Rise Time		--	47	--	ns
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		--	15	--	ns
T_{f}	Turn-Off Fall Time		--	6.4	--	ns

Source- Drain Diode Characteristics@ $T_j = 25^\circ\text{C}$ (unless otherwise stated)

V_{SD}	Forward on voltage	$I_{\text{SD}}=20\text{A}, V_{\text{GS}}=0\text{V}$	--	0.8	1.2	V
T_{rr}	Reverse Recovery Time	$I_{\text{SD}}=20\text{A}, V_{\text{GS}}=0\text{V}$	--	6.1	--	ns
Q_{rr}	Reverse Recovery Charge		$dI/dt=100\text{A}/\mu\text{s}$	--	0.6	nC

NOTE: ① Repetitive rating; pulse width limited by max junction temperature.

② Limited by T_{Jmax} , starting $T_j = 25^\circ\text{C}$, $L = 0.1\text{mH}$, $R_g = 25\Omega$, $I_{\text{AS}} = 20\text{A}$, $V_{\text{GS}} = 10\text{V}$. Part not recommended for use above this value

③ The power dissipation P_{DSM} is based on $R_{\theta_{\text{JA}}}$ and the maximum allowed junction temperature of 150°C .

④ Pulse width $\leq 380\mu\text{s}$; duty cycles $\leq 2\%$

Typical Characteristics

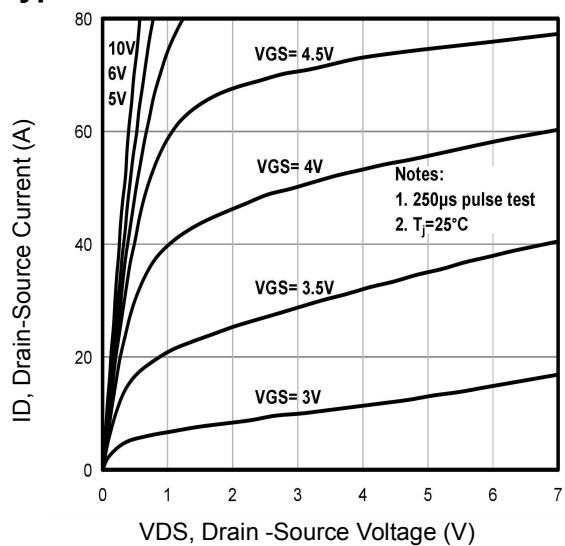


Fig1. Typical Output Characteristics

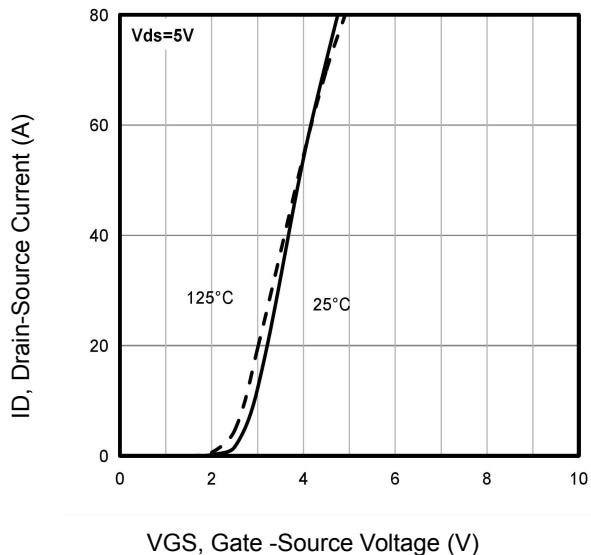


Fig3. Typical Transfer Characteristics

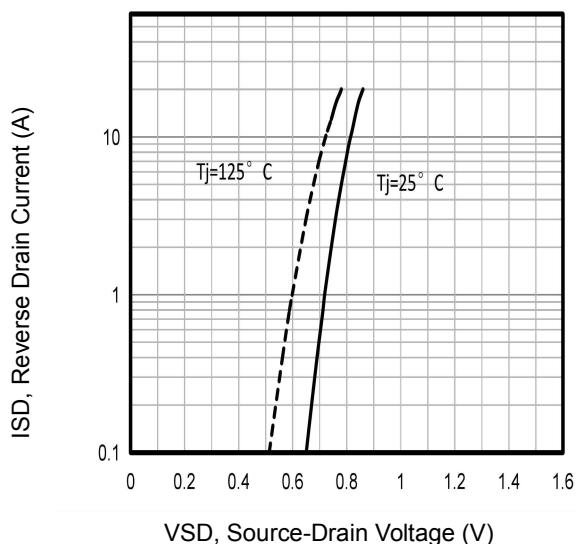


Fig5. Typical Source-Drain Diode Forward Voltage

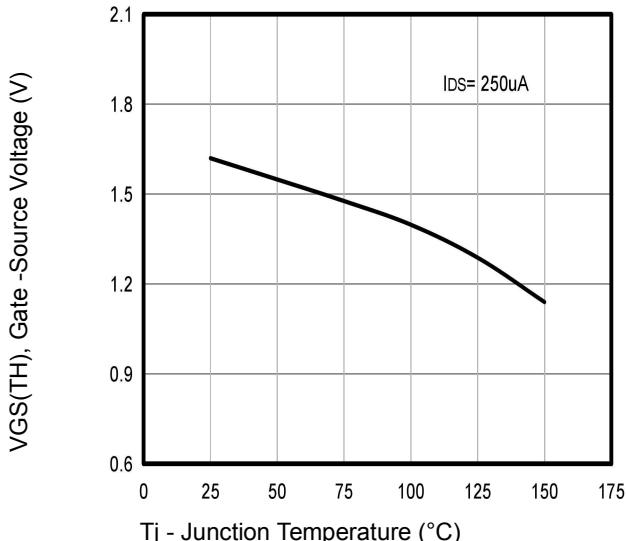


Fig2. $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

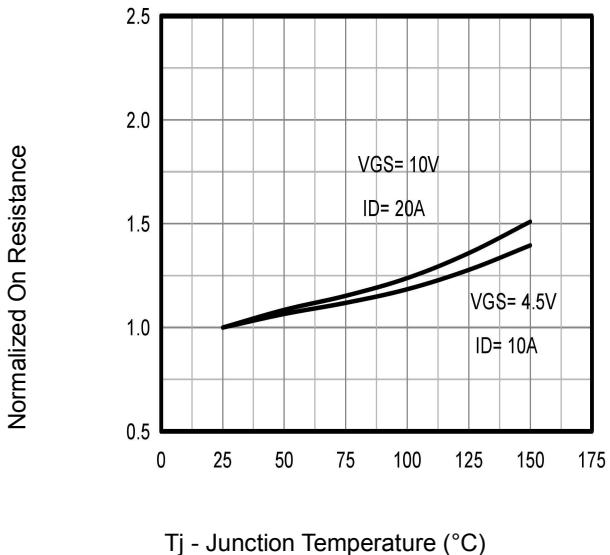


Fig4. Normalized On-Resistance Vs. T_j

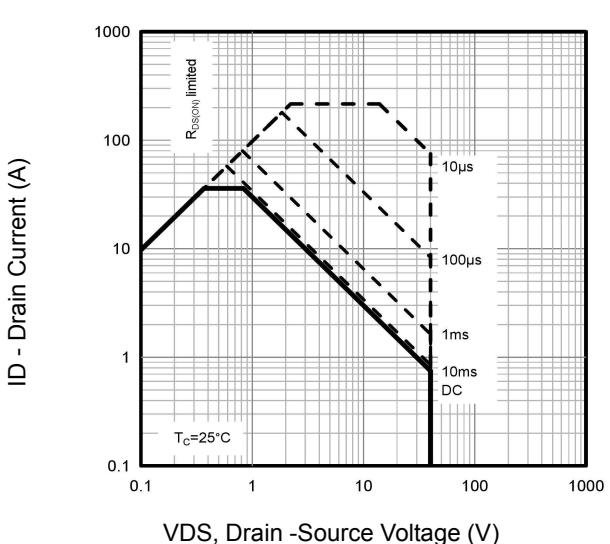
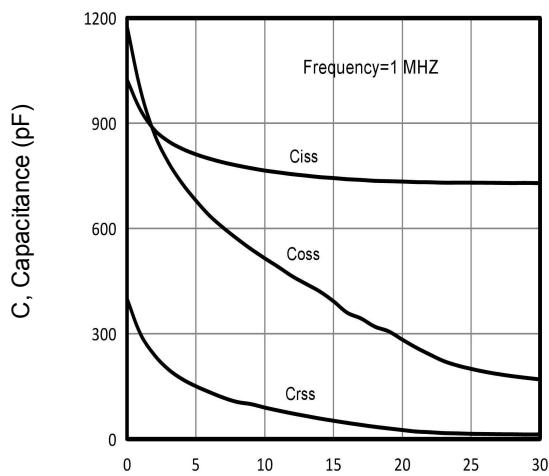


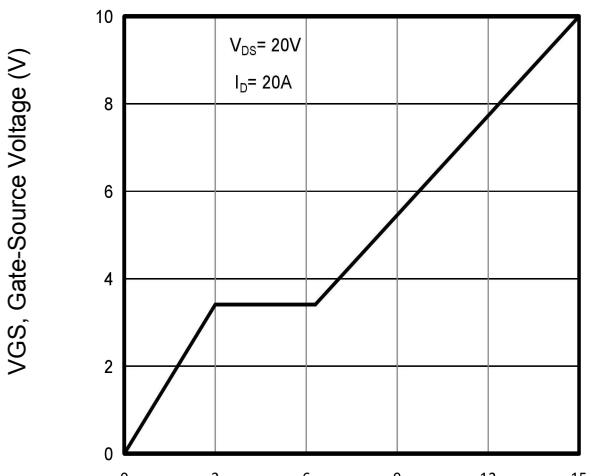
Fig6. Maximum Safe Operating Area

Typical Characteristics



V_{DS}, Drain-Source Voltage (V)

Fig7. Typical Capacitance Vs. Drain-Source Voltage



Q_g - Total Gate Charge (nC)

Fig8. Typical Gate Charge Vs. Gate-Source Voltage

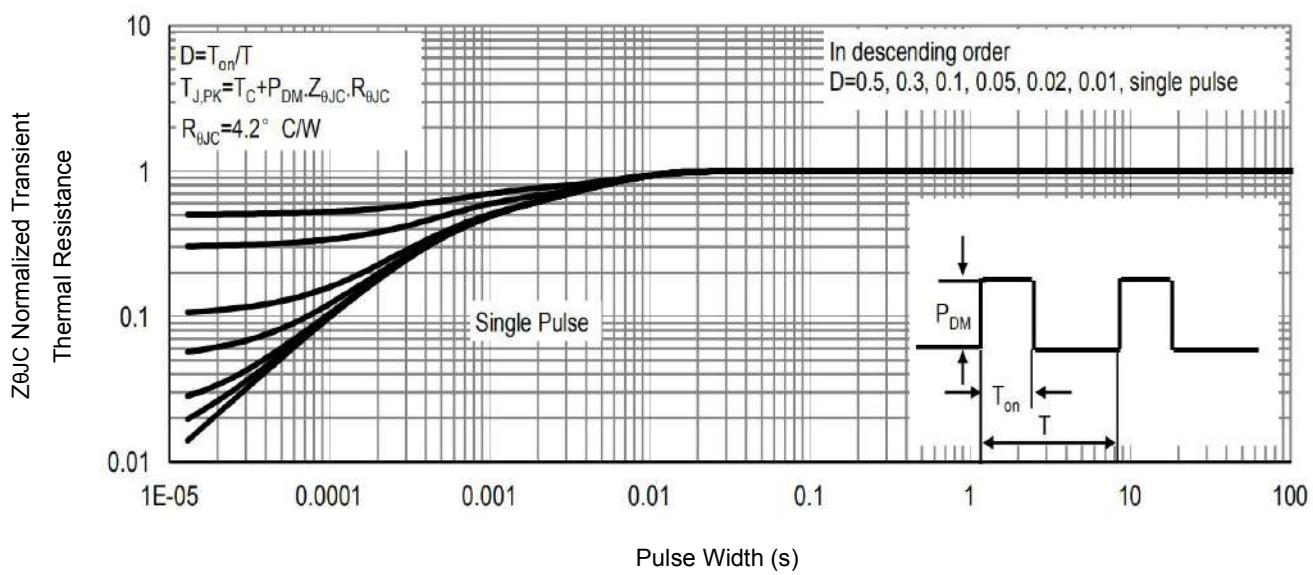


Fig9. Normalized Maximum Transient Thermal Impedance

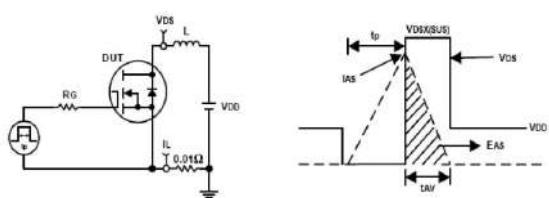


Fig10. Unclamped Inductive Test Circuit and waveforms

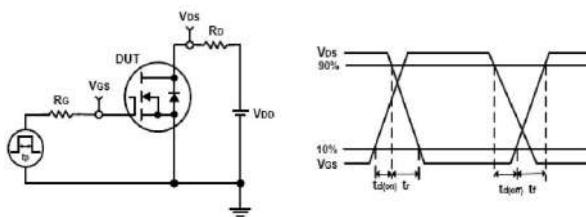
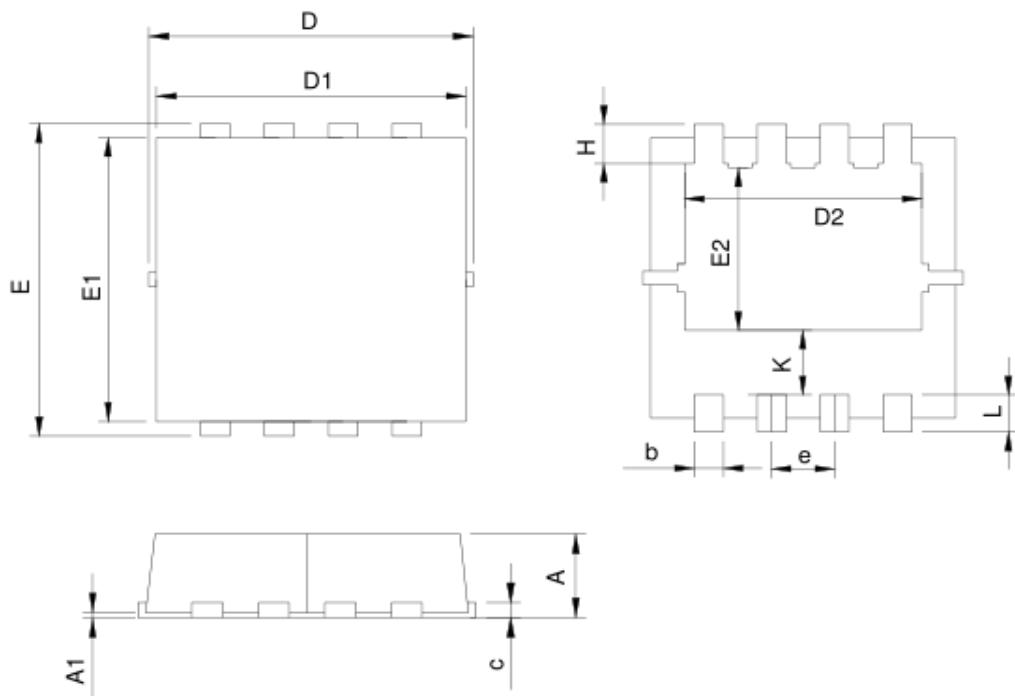


Fig11. Switching Time Test Circuit and waveforms

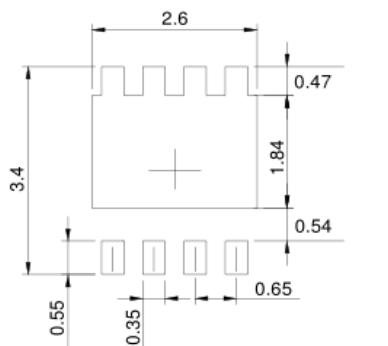
Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM40R065NE-R	40R065N	PDFN3.3*3.3-8	Tape&Reel	5000/Reel

PACKAGE	MARKING
PDFN3.3x3.3-8	 AS □□□ → Lot Number 40R065N □□□ → Date Code

Dimensions(PDFN3.3x3.3-8)


SYMBOL	DFN3.3x3.3-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	1.00	0.028	0.039
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
c	0.14	0.20	0.006	0.008
D	3.10	3.50	0.122	0.138
D1	3.05	3.25	0.120	0.128
D2	2.35	2.55	0.093	0.100
E	3.10	3.50	0.122	0.138
E1	2.90	3.10	0.114	0.122
E2	1.64	1.84	0.065	0.072
e	0.65 BSC		0.026 BSC	
H	0.32	0.52	0.013	0.020
K	0.59	0.79	0.023	0.031
L	0.25	0.55	0.010	0.022

RECOMMENDED LAND PATTERN


UNIT: mm

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