

Features

- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low RDS(ON)

Applications

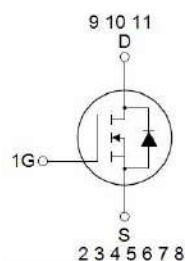
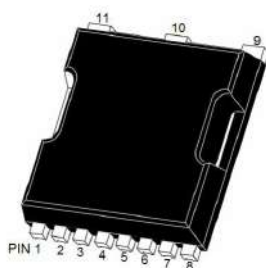
- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

Product Summary



VDS	40	V
RDS(on),max.@ VGS=10V	1.5	mΩ
ID	200	A

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■ Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V _{DS}	40	V
Gate-source Voltage		V _{GS}	±20	V
Drain Current (Silicon limited)		I _D	225	A
Drain Current ^A	T _C =25°C	I _D	200	A
	T _C =100°C		82	
Pulsed Drain Current ^B		I _{DM}	600	A
Avalanche energy ^C		E _{AS}	450	mJ
Total Power Dissipation ^D		P _D	114	W
Thermal Resistance Junction-to-Case		R _{θJC}	1.1	°C/W
Thermal Resistance Junction-to-Ambient ^E		R _{θJA}	20	
Junction and Storage Temperature Range		T _J , T _{STG}	-55~+150	°C

■ Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	40			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=40V, V_{GS}=0V$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}= \pm 20V, V_{DS}=0V$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}= V_{GS}, I_D=250\mu A$	1.2		2.5	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$			1.5	m Ω
		$V_{GS}=4.5V, I_D=20A$			2.3	
Gate Resistance	R_g	$V_{GS}=0V, V_{DS}$ Open, $f=1MHz$		2.7		Ω
Maximum Body-Diode Continuous Current	I_S				200	A
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, f=300KHz$		8300		pF
Output Capacitance	C_{oss}			1510		
Reverse Transfer Capacitance	C_{rss}			130		
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS}=10V, V_{DS}=32V, I_D=20A$		127		nC
Gate-Source Charge	Q_{gs}			35		
Gate-Drain Charge	Q_{gd}			26		
Reverse Recovery Chrage	Q_{rr}	$I_F=25A, di/dt=100A/us$		163		ns
Reverse Recovery Time	t_{rr}			100		
Turn-on Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DD}=20V, I_D=25A$ $R_{GEN}=2\Omega$		22.5		
Turn-on Rise Time	t_r			6.7		
Turn-off Delay Time	$t_{d(off)}$			80.3		
Turn-off fall Time	t_f			26.9		

Note:

- The maximum current rating is package limited.
- Repetitive rating; pulse width limited by max. junction temperature.
- $V_{DD}=32V, R_G=25\Omega, L=0.5\text{mH}$, starting $T_J=25^{\circ}\text{C}$.
- P_D is based on max. junction temperature, using junction-case thermal resistance.
- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a=25^{\circ}\text{C}$.

Typical Performance Characteristics

Figure.1 Typical Output Characteristics

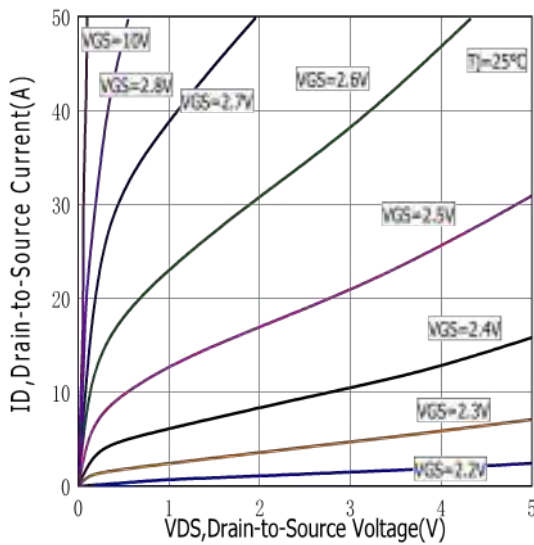


Figure.2 Typical Gate Charge vs Gate to Source Voltage

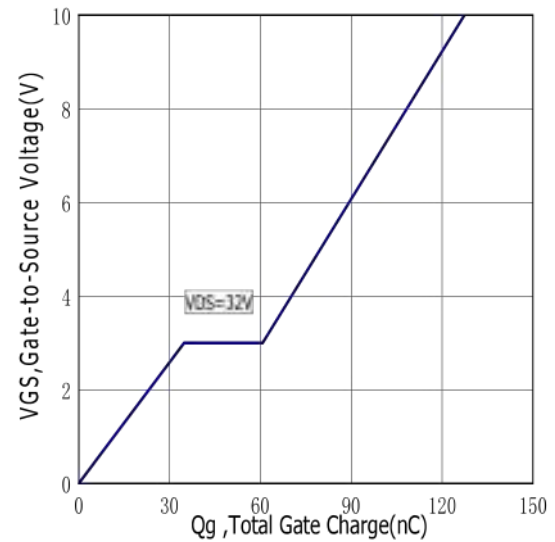


Figure.3 Typical Body Diode Transfer Characteristics

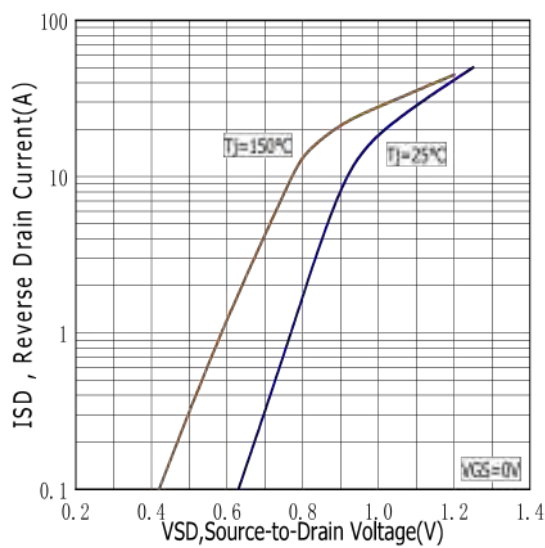
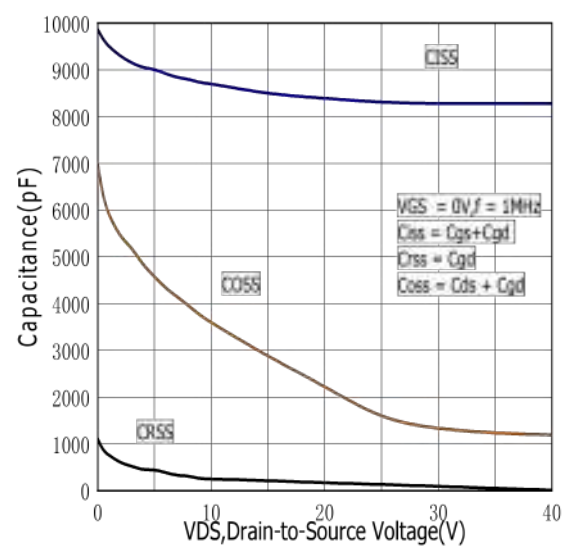


Figure.4 Typical Capacitance vs Drain to Source Voltage



Typical Performance Characteristics

Figure.5 Typical Breakdown Voltage vs Junction Temperature

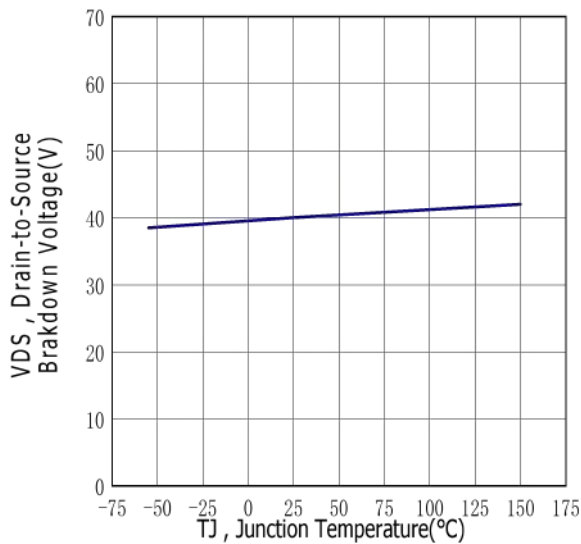


Figure.6 Typical Drain to Source on Resistance vs Junction Temperature

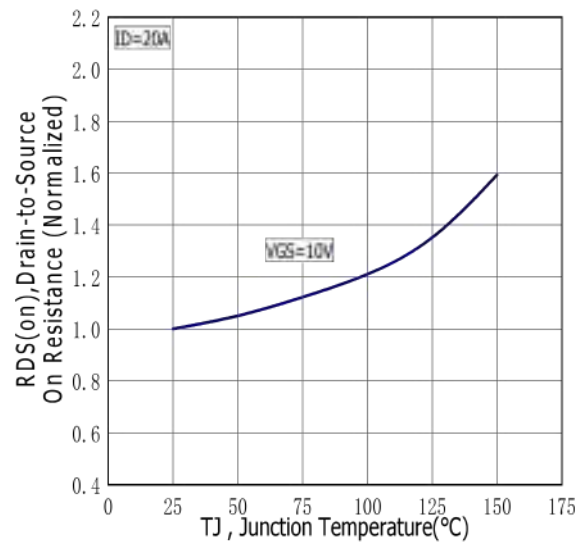


Figure.7 Maximum Forward Bias Safe Operating Area

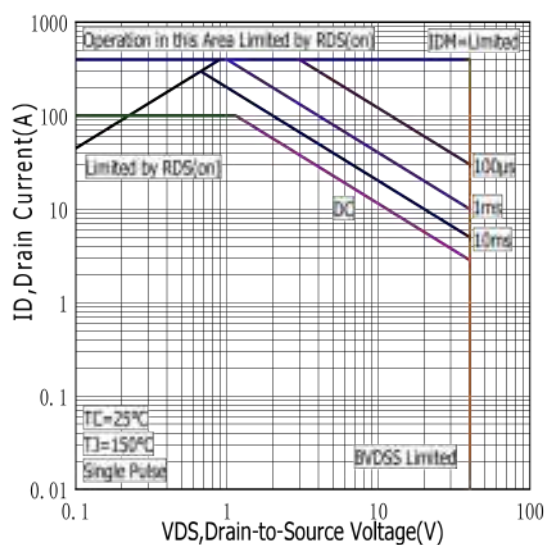
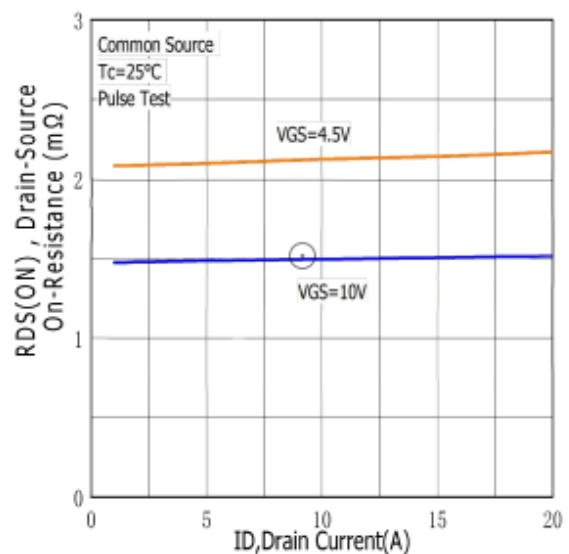


Figure.8 Typical Drain to Source ON Resistance vs Drain Current



Typical Performance Characteristics

Figure.9 Maximum EAS vs Channel Temperature

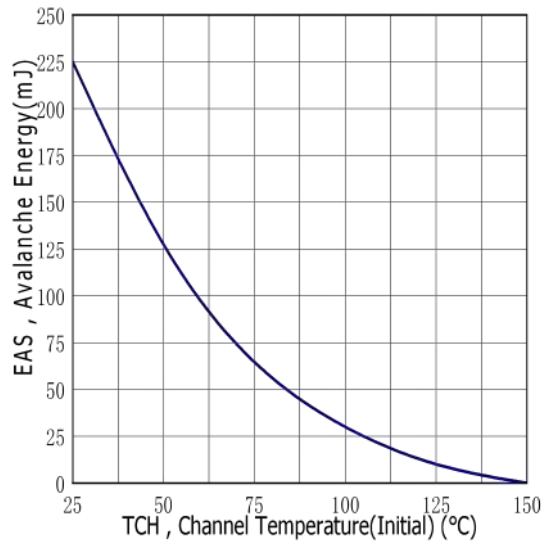


Figure.10 Typical Threshold Voltage vs Case Temperature

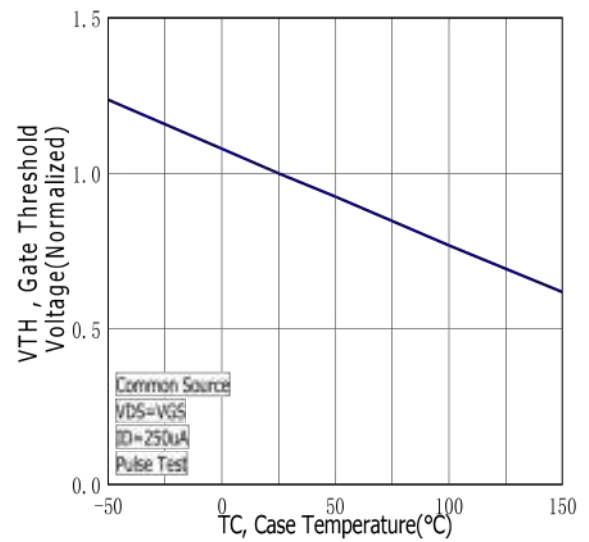


Figure.11 Typical Transfer Characteristics

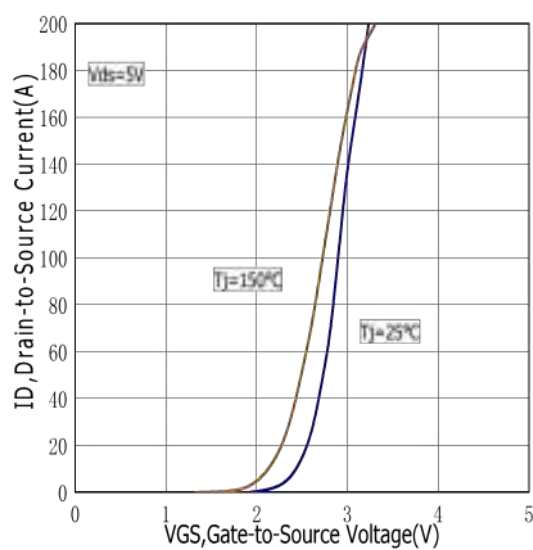


Figure.12 Maximum Power Dissipation vs Case Temperature

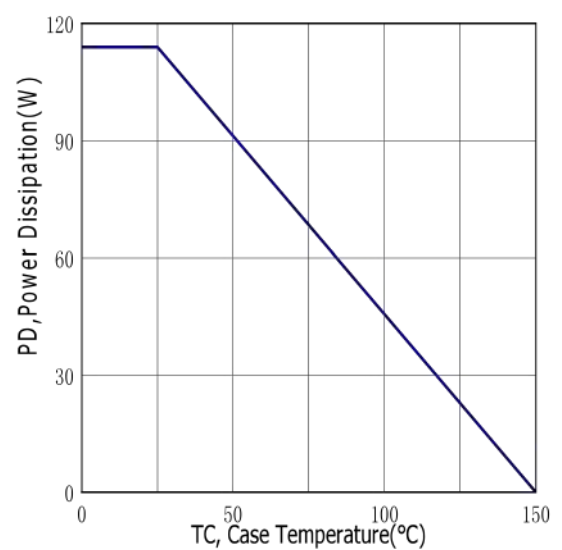
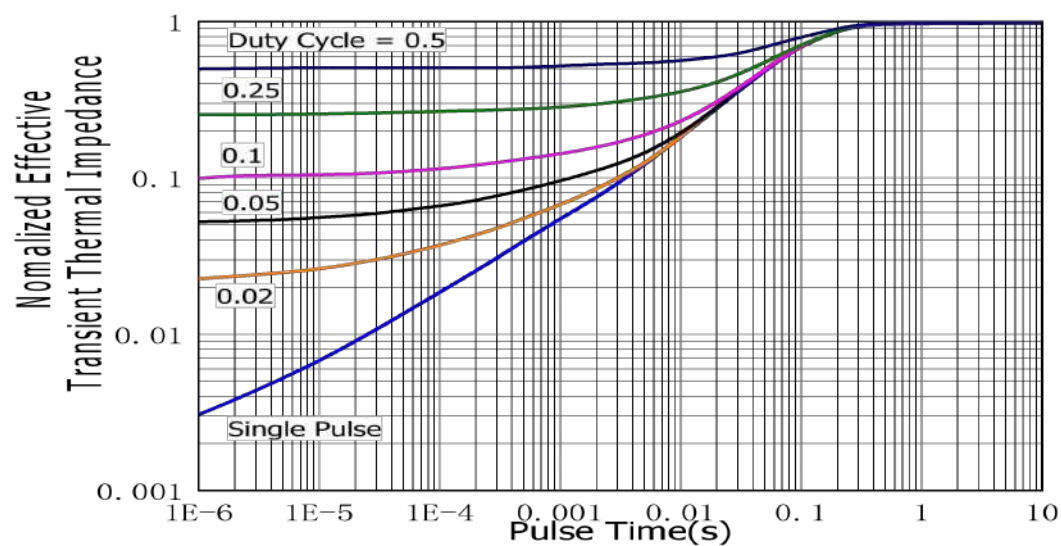


Figure.13 Maximum Effective Thermal Impedance , Junction to Case



Test circuits and waveforms

Figure A: Gate Charge Test Circuit & Waveforms

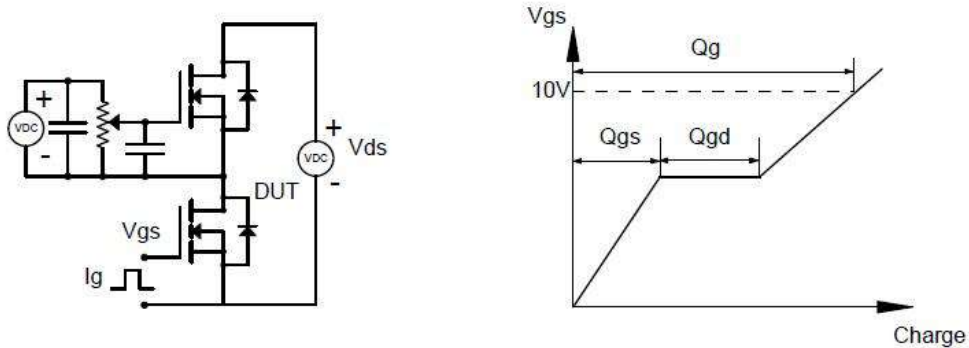


Figure B: Resistive Switching Test Circuit & Waveforms

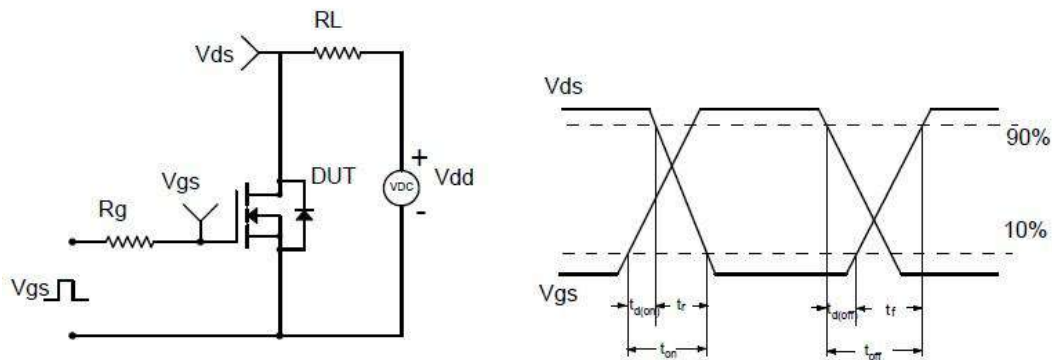


Figure C: Unclamped Inductive Switching (UIS) Test

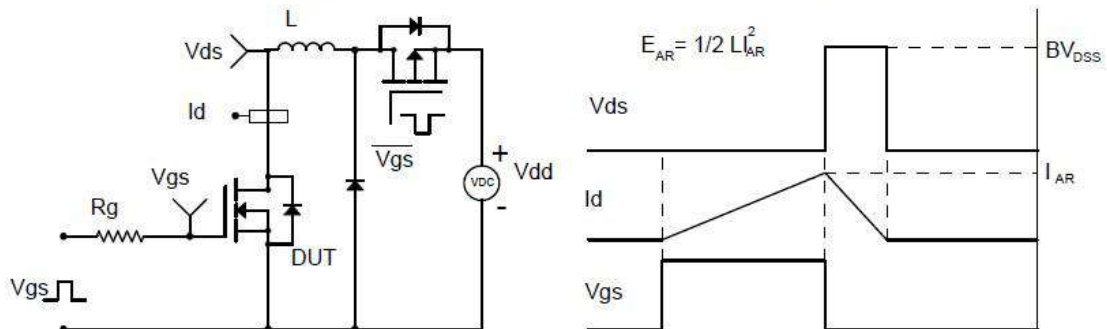
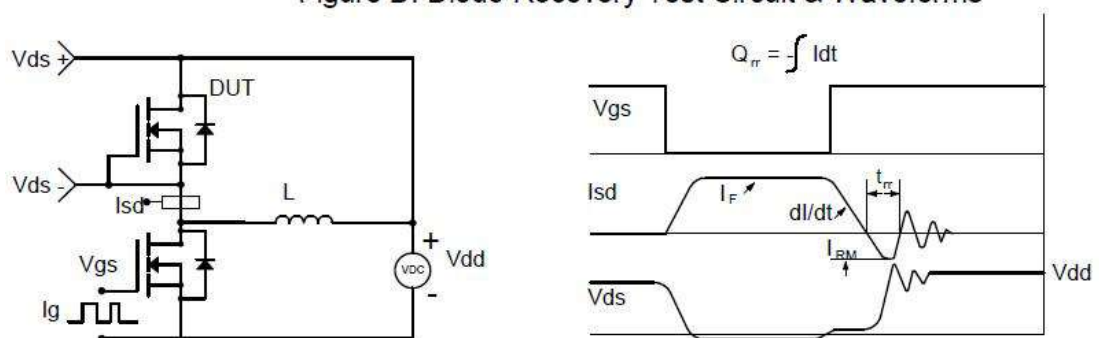
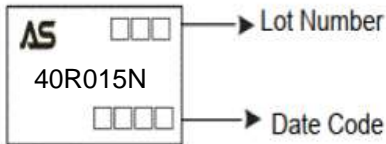


Figure D: Diode Recovery Test Circuit & Waveforms

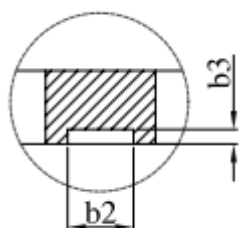
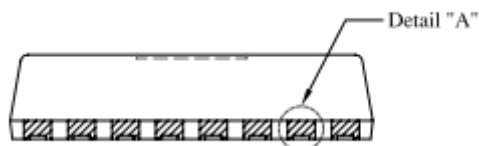
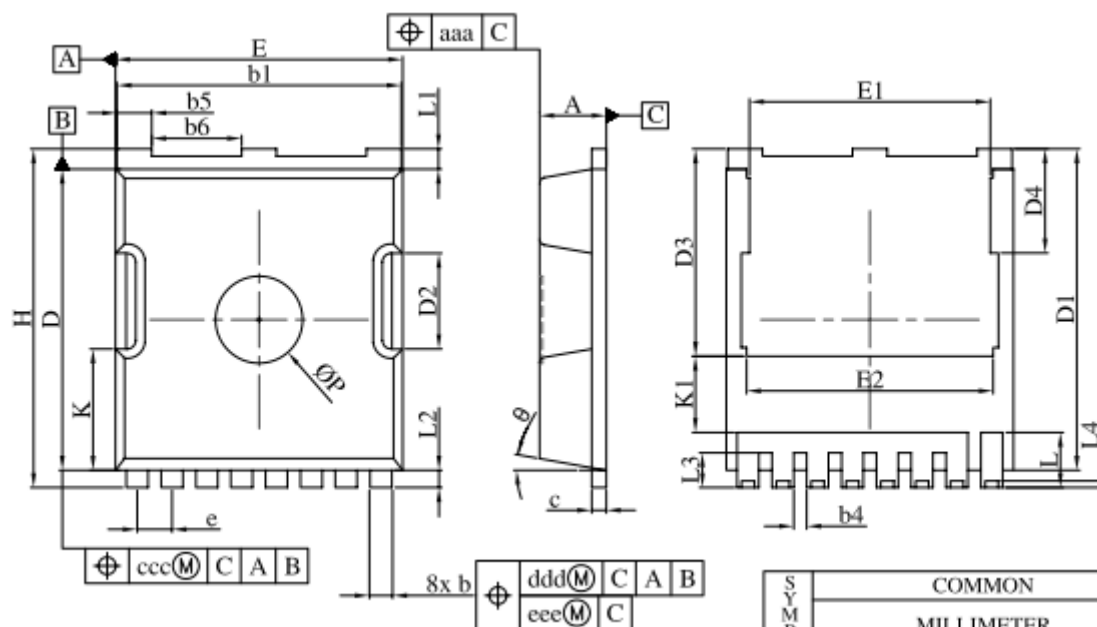


Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM40R015NT-R	40R015N	TOLL	Tape&Reel	2000/Reel

PACKAGE	MARKING
TOLL	 <p>AS □□□ → Lot Number</p> <p>40R015N</p> <p>□□□□ → Date Code</p>

TOLL



SYMBOL	COMMON		
	MILLIMETER		
	MIN.	NOMINAL	MAX.
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.36	0.45	0.55
b3	0.05	0.100	0.35
b4	0.30	0.40	0.50
b5	1.10	1.20	1.30
b6	3.00	3.10	3.20
c	0.40	0.50	0.60
D	10.28	10.38	10.55
D1	10.98	11.08	11.18
D2	3.20	3.30	3.40
D3	7.00	7.15	7.30
D4	3.44	3.59	3.74
e	1.10	1.20	1.30
E	9.80	9.90	10.00
E1	8.20	8.30	8.40
E2	8.35	8.50	8.65
H	11.50	11.68	11.85
K	4.08	4.18	4.28
K1	2.45	---	---
L	1.60	1.90	2.10
L1	0.50	0.70	0.90
L2	0.50	0.60	0.70
L3	1.00	1.20	1.30
L4	0.13	0.23	0.33
P	2.85	3.00	3.15
θ	10° REF		
aaa	0.20		
ccc	0.20		
ddd	0.25		
eee	0.20		

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