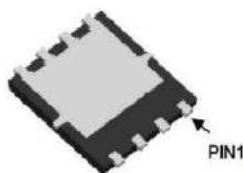
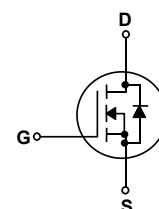
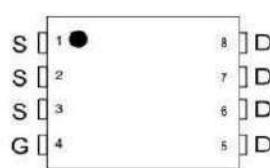


General Features

- Excellent gate charge $\times R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 150°C operating temperature
- Pb-free lead plating

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification


DFN5*6-8

N-Channel

Product Summary

V_{DS}	120	V
$R_{DS(on),Typ} @ V_{GS}=10\text{ V}$	4.3	$\text{m}\Omega$
I_D	90	A



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	120	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	90	A
Drain Current-Continuous($T_c=100^\circ\text{C}$)	$I_D (100^\circ\text{C})$	64	A
Pulsed Drain Current	I_{DM}	360	A
Maximum Power Dissipation	P_D	130	W
Derating factor		1.04	W/ $^\circ\text{C}$
Single pulse avalanche energy (Note 4)	E_{AS}	400	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ\text{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.92	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62	$^\circ\text{C}/\text{W}$

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	120	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=96\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	±100	nA
On Characteristics <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.8	2.5	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=45\text{A}$	-	4.3	6.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=45\text{A}$		5.7	7.8	
Forward Transconductance	g_{FS}	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=50\text{A}$		60	-	S
Dynamic Characteristics <small>(Note 3)</small>						
Input Capacitance	C_{iss}	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	3722	-	pF
Output Capacitance	C_{oss}		-	228	-	pF
Reverse Transfer Capacitance	C_{rss}		-	25	-	pF
Switching Characteristics <small>(Note 3)</small>						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=60\text{V}, I_{\text{D}}=45\text{A}$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=1.6\Omega$	-	20	-	nS
Turn-on Rise Time	t_r		-	15	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	40	-	nS
Turn-Off Fall Time	t_f		-	10	-	nS
Total Gate Charge	Q_g	$V_{\text{DS}}=60\text{V}, I_{\text{D}}=45\text{A}, V_{\text{GS}}=10\text{V}$	-	90	-	nC
Gate-Source Charge	Q_{gs}		-	21	-	nC
Gate-Drain Charge	Q_{gd}		-	23.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <small>(Note 2)</small>	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=45\text{A}$	-	-	1.2	V
Diode Forward Current	I_{S}		-	-	90	A
Reverse Recovery Time	t_{rr}	$T_J = 25^\circ\text{C}, I_F = 45\text{A}$ $dI/dt = 100\text{A}/\mu\text{s}$ <small>(Note 3)</small>	-	70	-	nS
Reverse Recovery Charge	Q_{rr}		-	137	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
3. Guaranteed by design, not subject to production
4. EAS condition : $T_J=25^\circ\text{C}, V_{\text{DD}}=50\text{V}, V_G=10\text{V}, L=0.5\text{mH}, R_g=25\Omega$

Typical Electrical and Thermal Characteristics

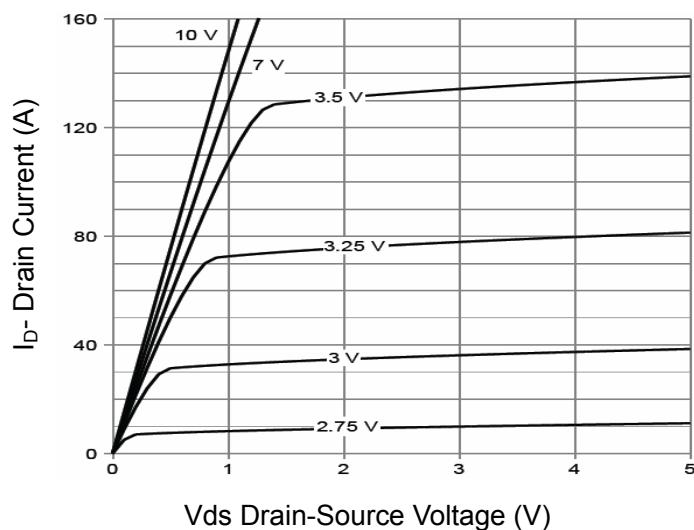


Figure 1 Output Characteristics

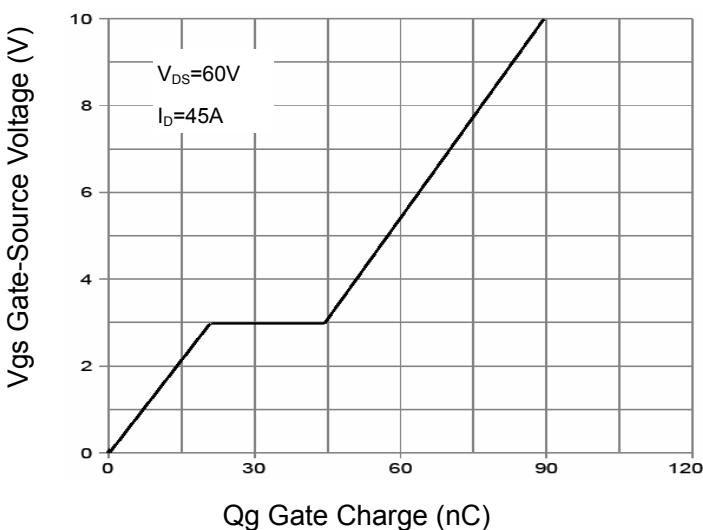


Figure 4 Gate Charge

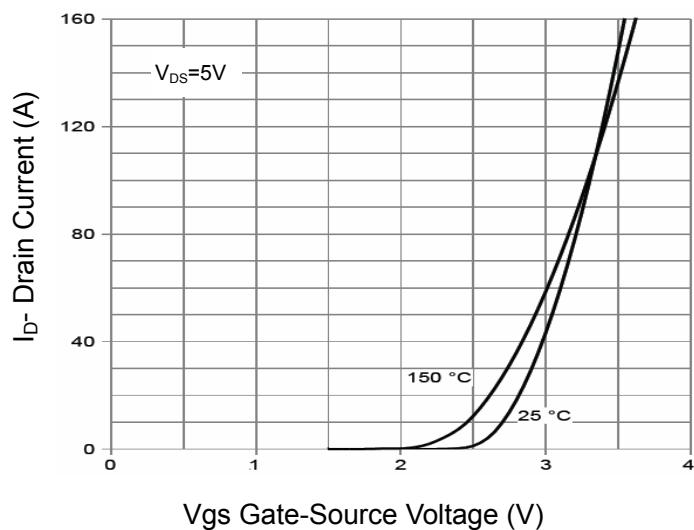


Figure 2 Transfer Characteristics

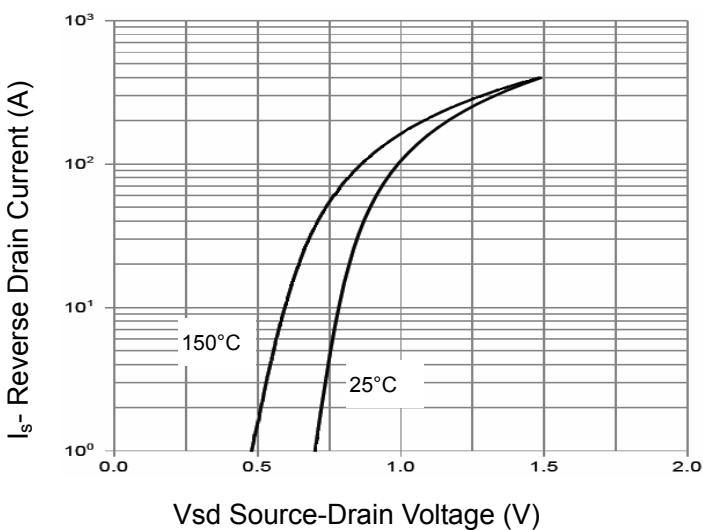


Figure 5 Source-Drain Diode Forward

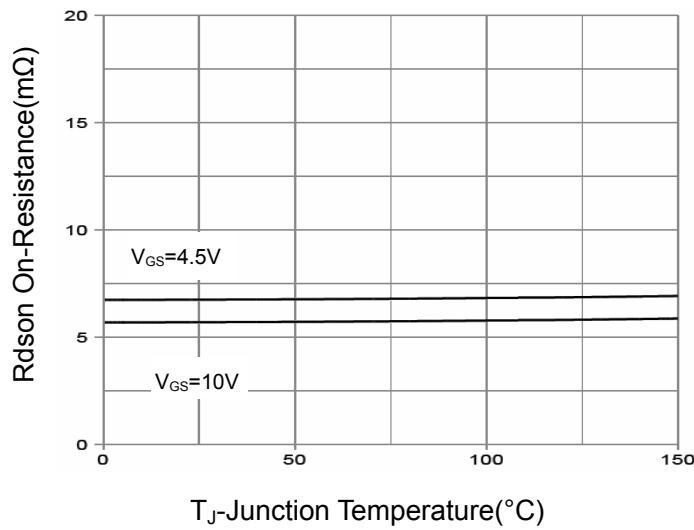


Figure 3 Rdson-Junction Temperature

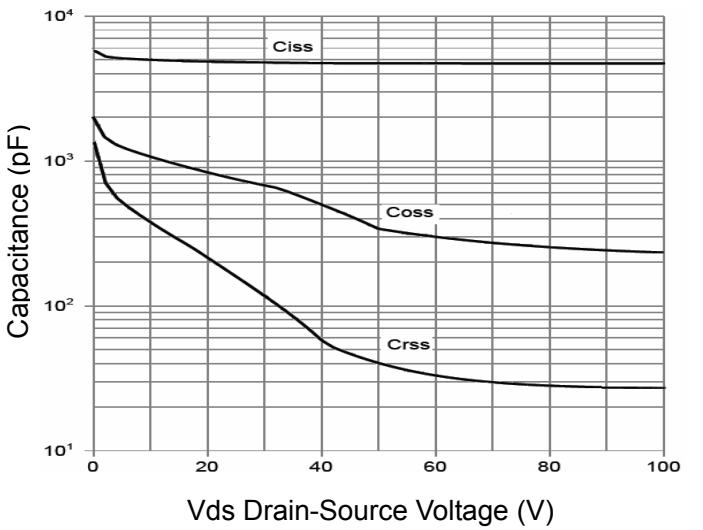


Figure 6 Capacitance vs Vds

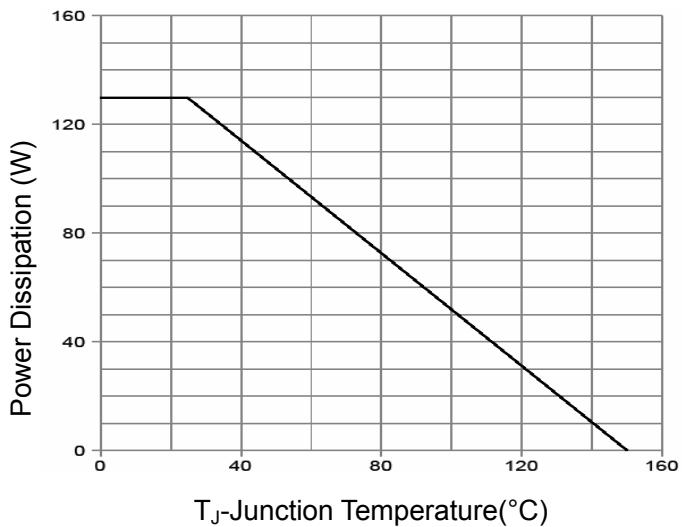


Figure 7 Power De-rating

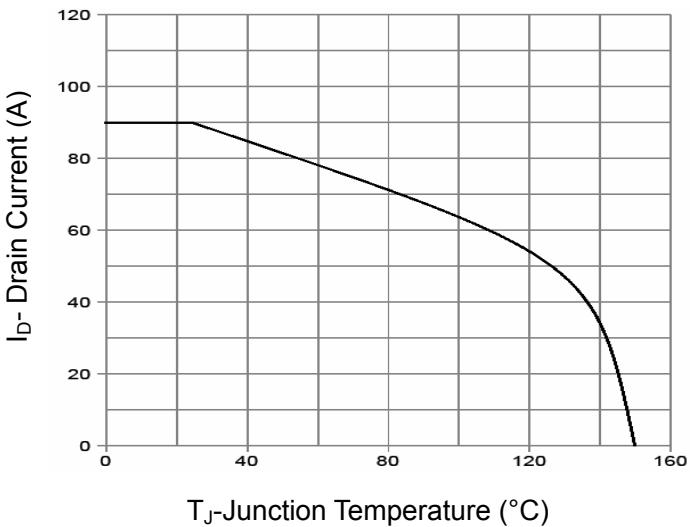


Figure 9 Current De-rating

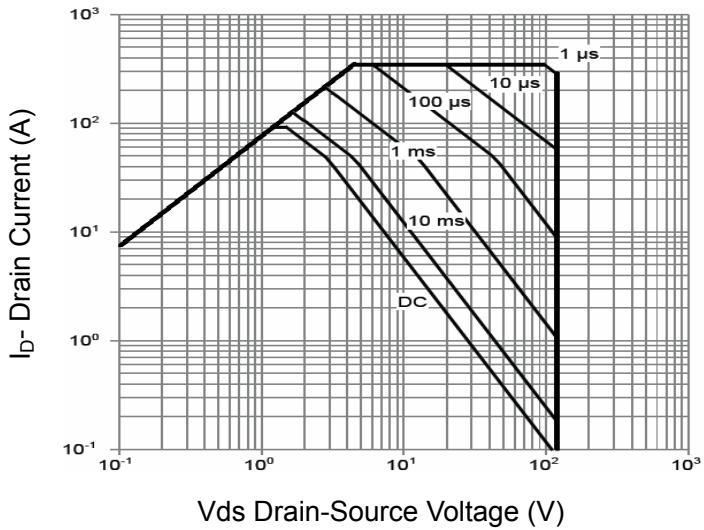


Figure 8 Safe Operation Area

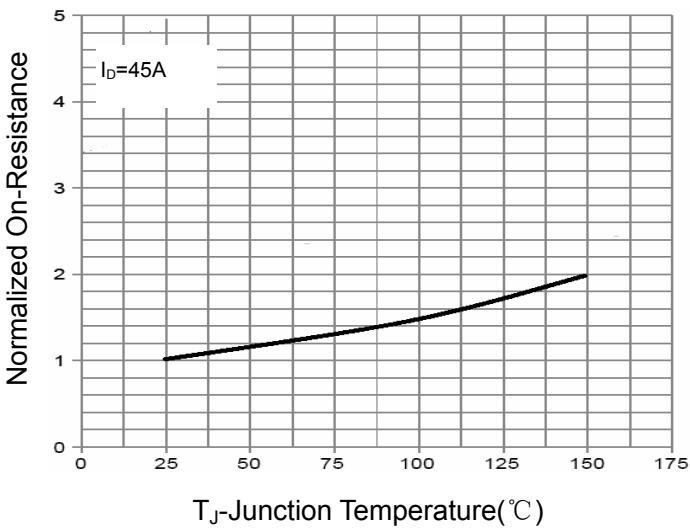


Figure 10 Rdson-Junction Temperature

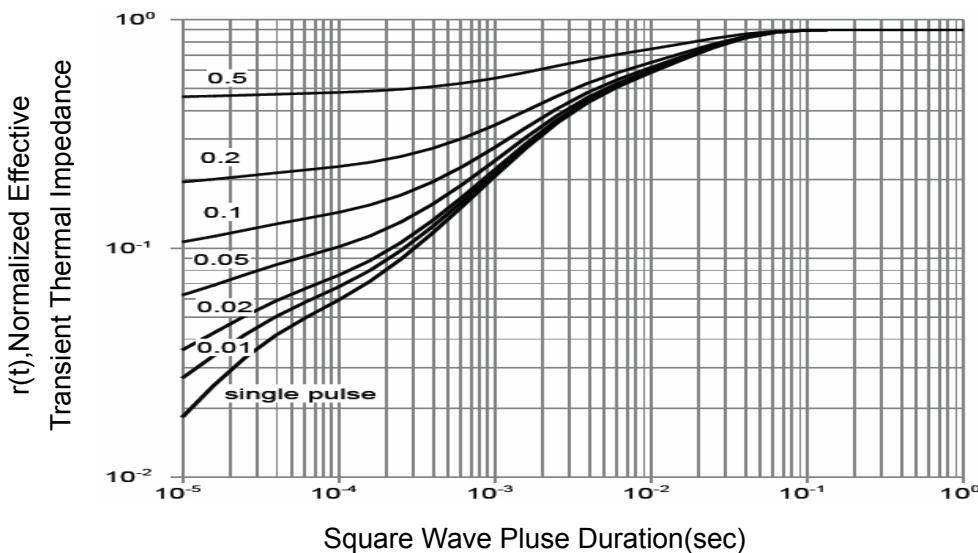
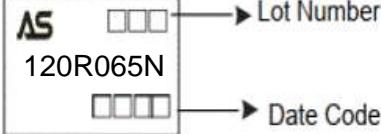
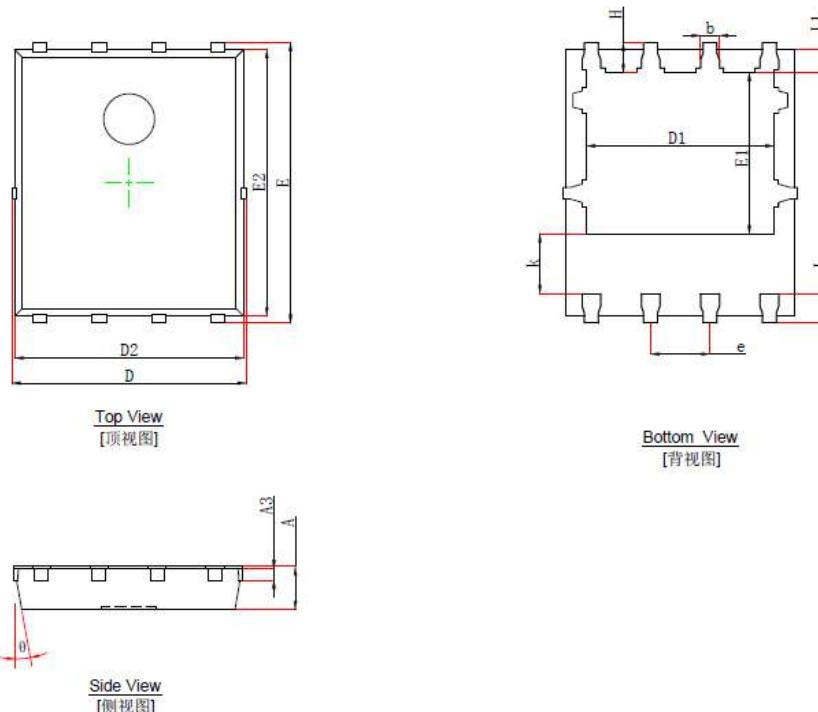


Figure 11 Normalized Maximum Transient Thermal Impedance

Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM120R065NQ-R	120R065N	PDFN5*6-8	Tape&Reel	4000/Reel

PACKAGE	MARKING
PDFN5*6-8	

PDFN5x6_P, 8 Leads


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

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