

## Features

- Advanced Trench Technology
- Excellent RDS(ON) and Low Gate Charge
- Lead free product is acquired

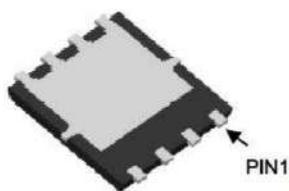
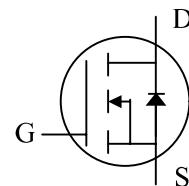
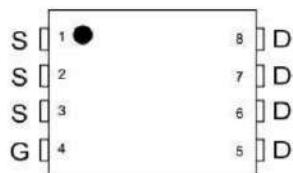
## Application

- Load Switch
- PWM Application
- Power management

## Product Summary



$V_{DS}$	20	V
$R_{DS(on),Max} @ V_{GS}=4.5V$	11.2	mΩ
$I_D$	16	A


**PDFN3.3x3.3-8**


## Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.	Units
$V_{DSS}$	Drain-Source Voltage	20	V
$V_{GSS}$	Gate-Source Voltage	$\pm 12$	V
$I_D$	Continuous Drain Current  $T_C = 25^\circ C$	16	A
		$T_C = 100^\circ C$	20
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>	64	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>	23	mJ
$P_D$	Power Dissipation  $T_C = 25^\circ C$	20	W
$R_{eJC}$	Thermal Resistance, Junction to Case	7.5	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +175	$^\circ C$

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	20	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=20\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 12\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	0.5	-	1.2	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=4.5\text{V}$ , $I_D=15\text{A}$	-	-	11.2	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$ , $I_D=10\text{A}$	-	-	17.5	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=10\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	1000	-	pF
$C_{oss}$	Output Capacitance		-	182	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	164	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=10\text{V}$ , $I_D=15\text{A}$ , $V_{GS}=4.5\text{V}$	-	15	-	nC
$Q_{gs}$	Gate-Source Charge		-	2	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	5.2	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=10\text{V}$ , $I_D=15\text{A}$ , $R_{\text{GEN}}=3\Omega$ , $V_{GS}=4.5\text{V}$	-	9	-	ns
$t_r$	Turn-on Rise Time		-	25	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	37	-	ns
$t_f$	Turn-off Fall Time		-	14	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	16	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	64	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=30\text{A}$	-	-	1.2	V

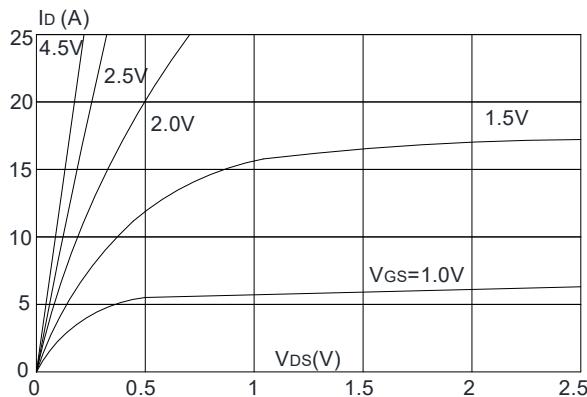
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition:  $T_J=25^\circ\text{C}$ ,  $V_{DD}=10\text{V}$ ,  $V_G=4.5\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_G=25\Omega$ ,  $I_{AS}=9.6\text{A}$

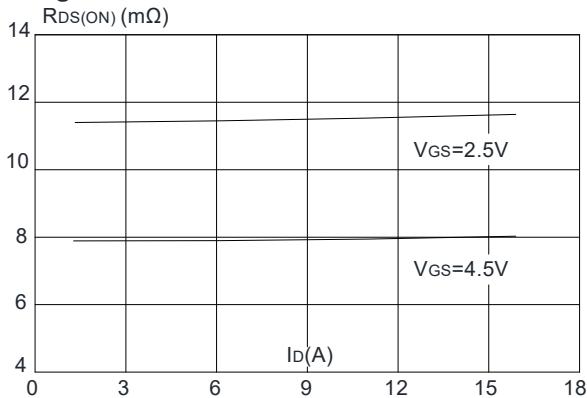
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 0.5\%$

## Typical Performance Characteristics

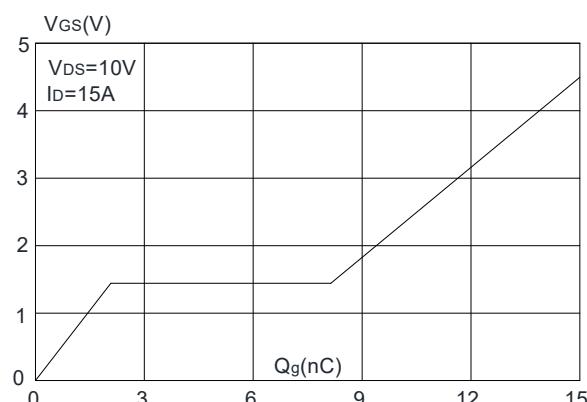
**Figure 1:** Output Characteristics



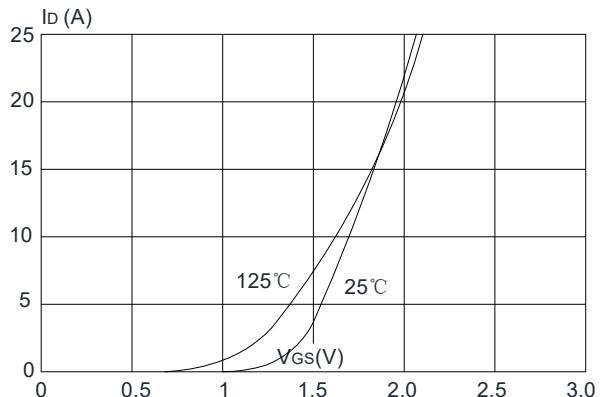
**Figure 3:** On-resistance vs. Drain Current



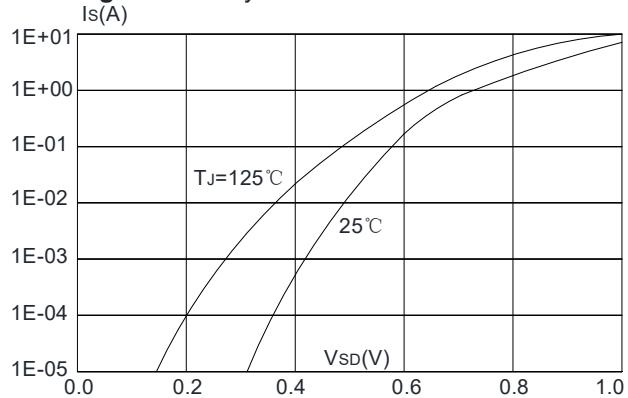
**Figure 5:** Gate Charge Characteristics



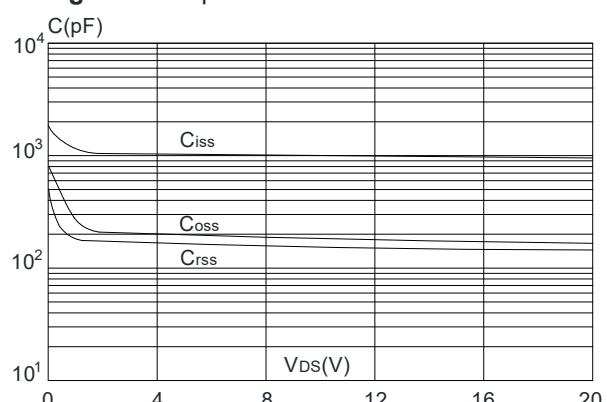
**Figure 2:** Typical Transfer Characteristics



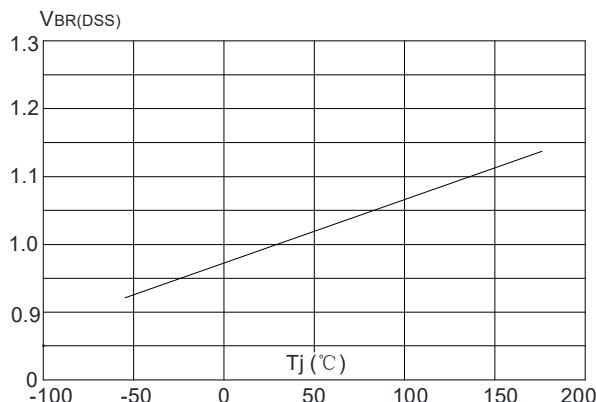
**Figure 4:** Body Diode Characteristics



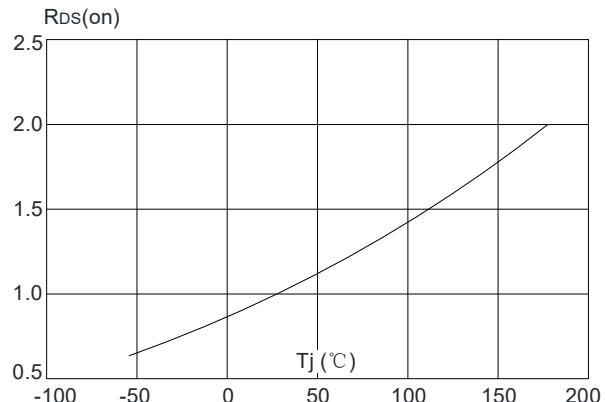
**Figure 6:** Capacitance Characteristics



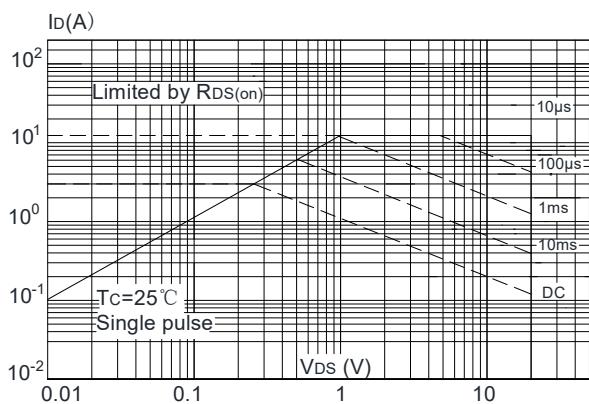
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



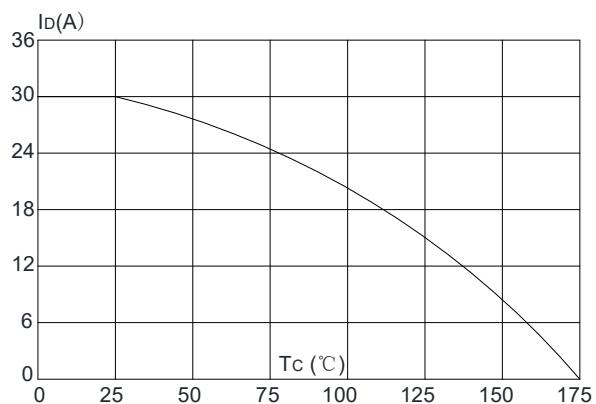
**Figure 8:** Normalized on Resistance vs. Junction Temperature



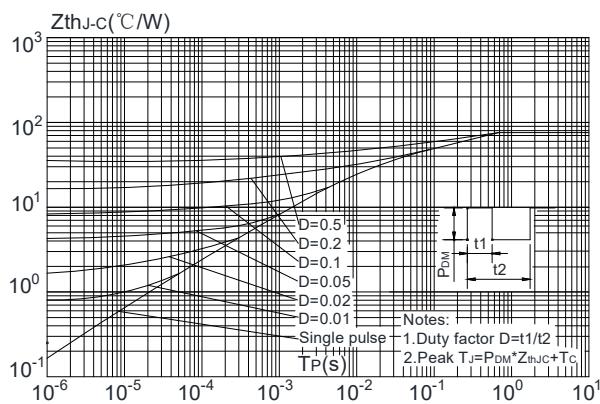
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



## Test Circuit

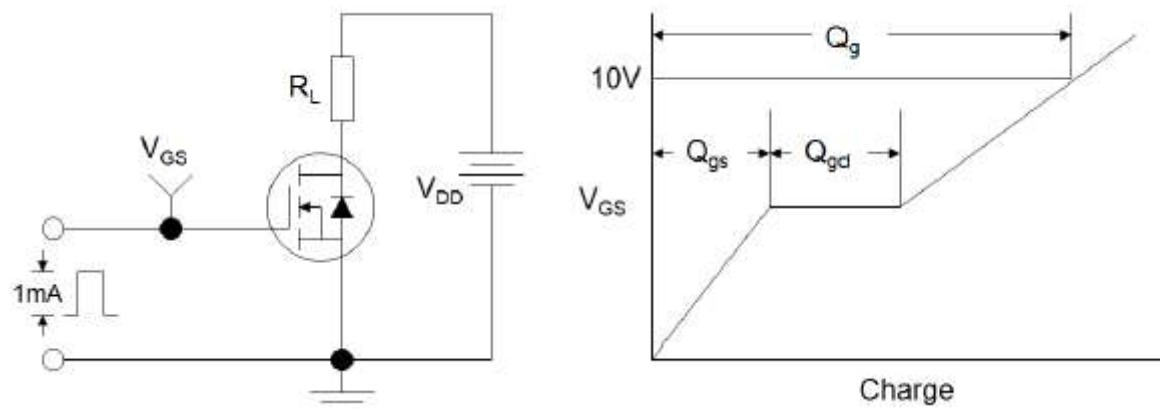


Figure1:Gate Charge Test Circuit & Waveform

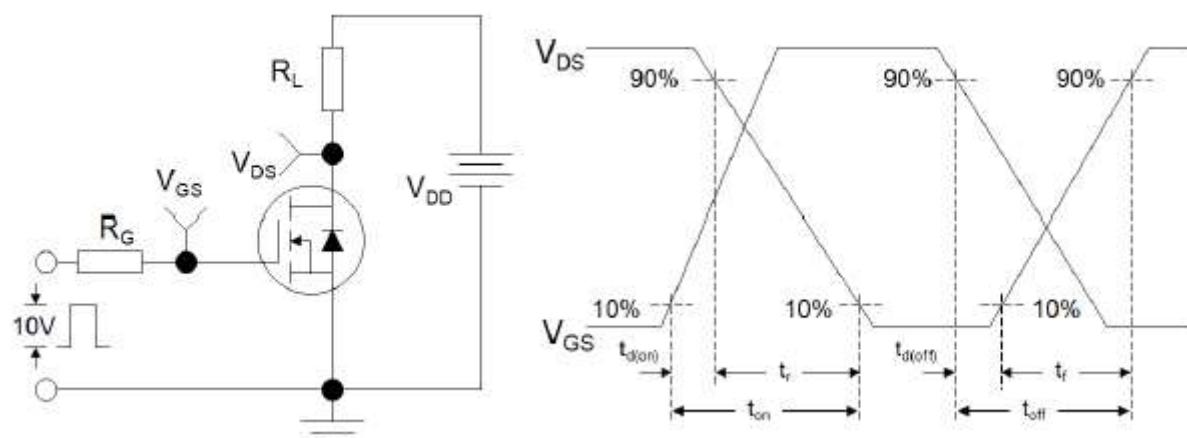


Figure 2: Resistive Switching Test Circuit & Waveforms

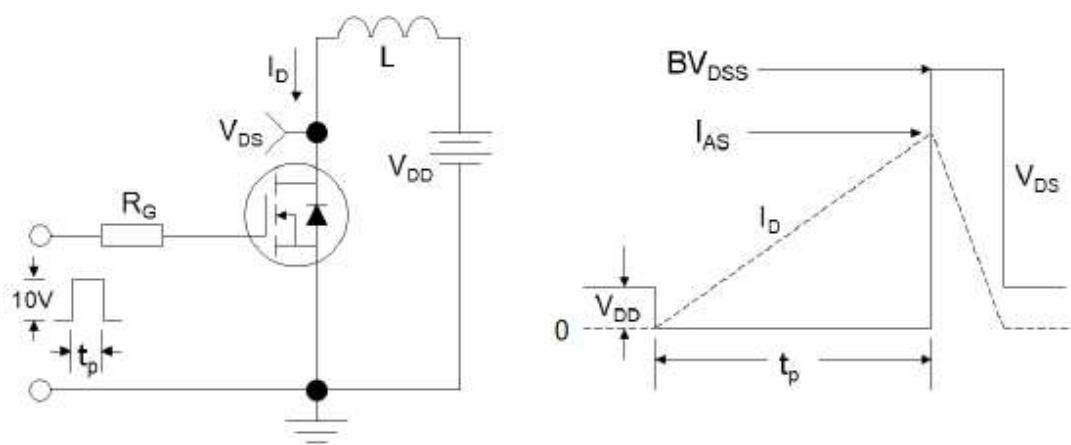
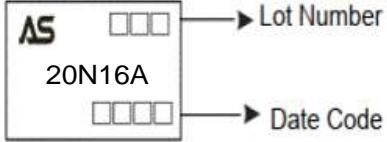
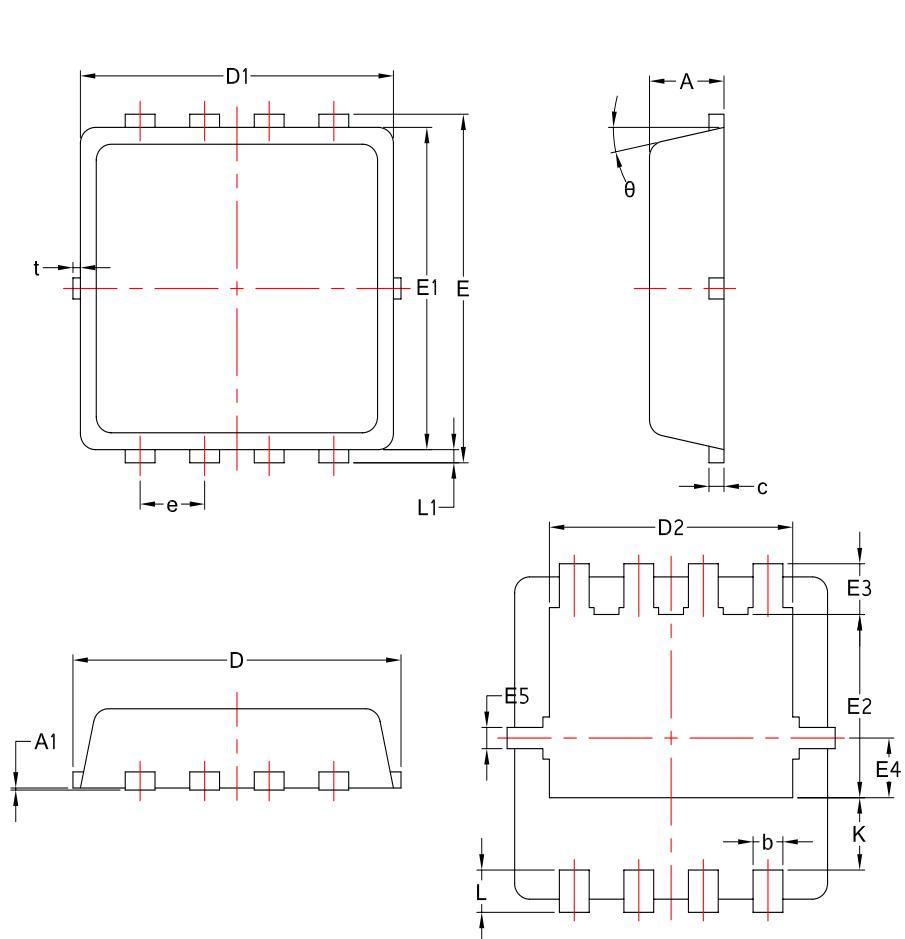


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

## Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM20N16AE-R	20N16A	PDFN3.3x3.3-8	Tape&Reel	5000/Reel

PACKAGE	MARKING
PDFN3.3x3.3-8	



SYMBOL	COMMON		
	MM		
	MIN	NOM	MAX
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.59	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
θ	10°	12°	14°

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