

## Features

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Green Device Available

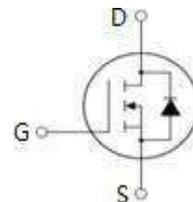
## Applications

- DC-DC Converter
- Motor Control
- Secondary Side Synchronous Rectification

## Product Summary



$V_{DS}$	60	V
$R_{DS(on), Typ @ V_{GS}=10 V}$	3.4	m $\Omega$
$I_D$	80	A



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C=25^\circ C$	Continuous Drain Current <sup>1,6</sup>	80	A
$I_D @ T_C=100^\circ C$	Continuous Drain Current <sup>1,6</sup>	50	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	240	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	101	mJ
$I_{AS}$	Avalanche Current	45	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation <sup>4</sup>	54.3	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	55	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	2.3	$^\circ C/W$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	---	---	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	3.4	4	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	---	4.5	5.5	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	1.7	2.5	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =20A	---	65	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	0.7	---	Ω
Q <sub>g</sub>	Total Gate Charge (10V)	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	58	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	16	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	4	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω, I <sub>D</sub> =20A	---	18	---	ns
T <sub>r</sub>	Rise Time		---	8	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	50	---	
T <sub>f</sub>	Fall Time		---	10.5	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1MHz	---	3530	---	pF
C <sub>oss</sub>	Output Capacitance		---	1554	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	22	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	40	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =20A, di/dt=100A/μs, T <sub>J</sub> =25°C	---	24	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	85	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=45A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.
- 6.The maximum current rating is package limited.

Typical Characteristics

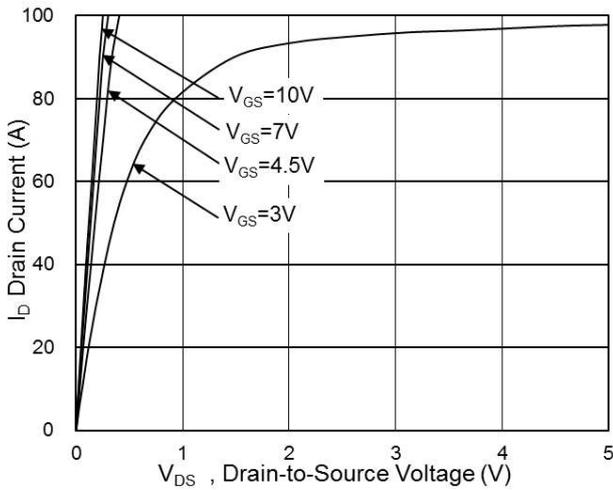


Fig.1 Typical Output Characteristics

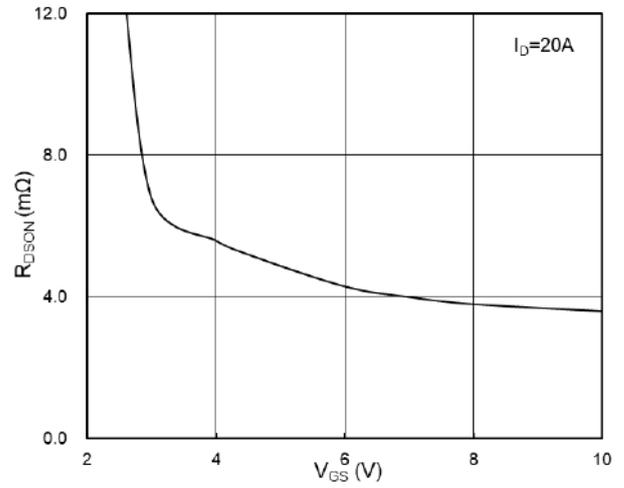


Fig.2 On-Resistance vs G-S Voltage

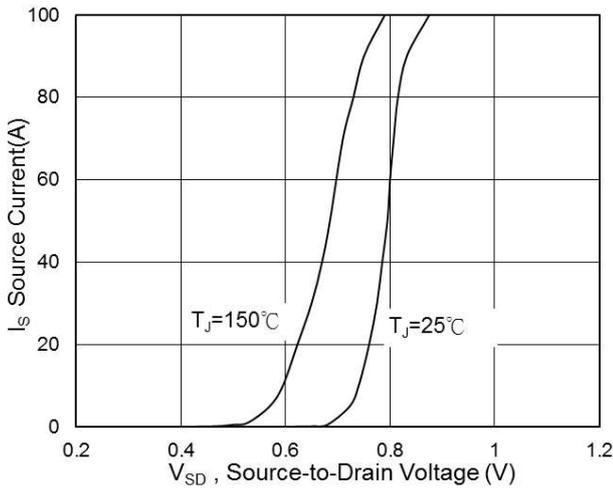


Fig.3 Diode Forward Voltage vs. Current

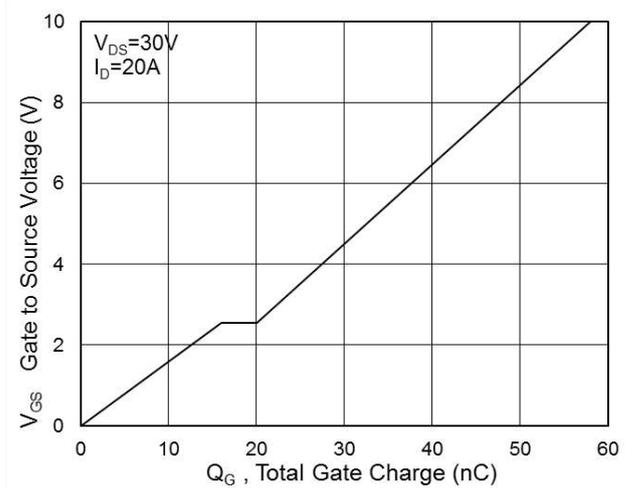


Fig.4 Gate-Charge Characteristics

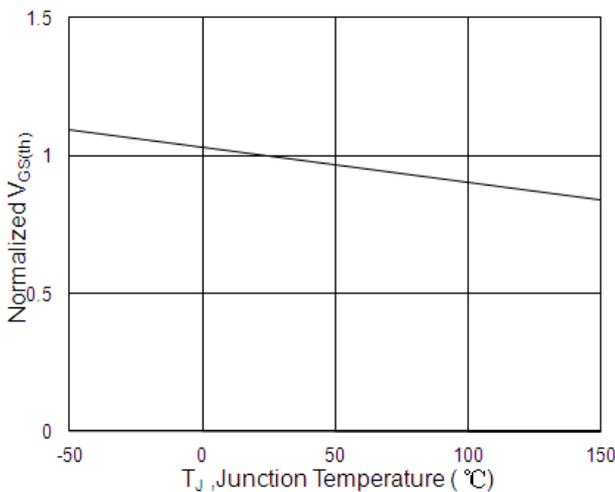


Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$

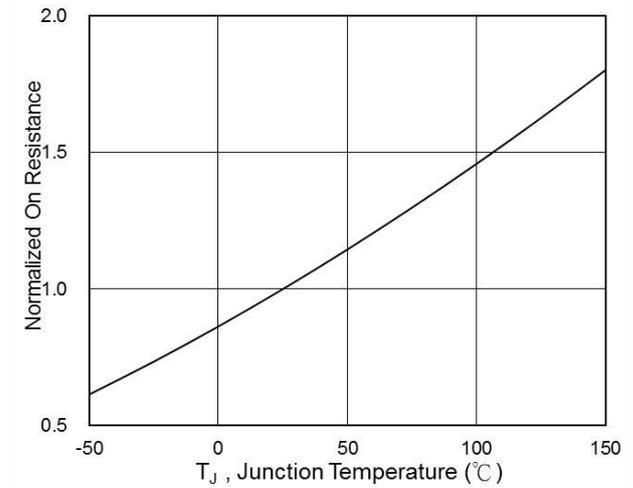
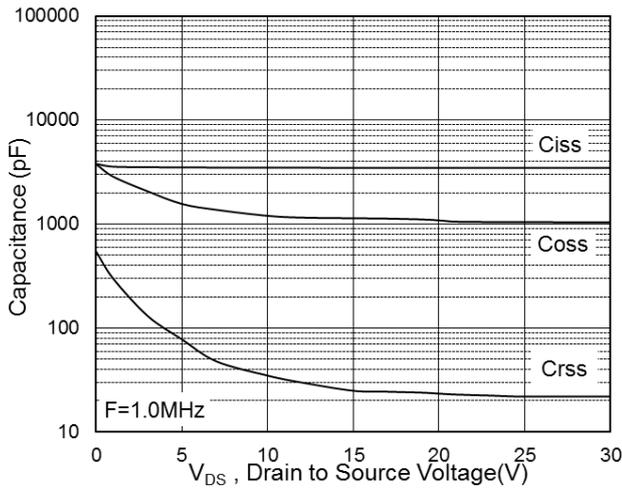
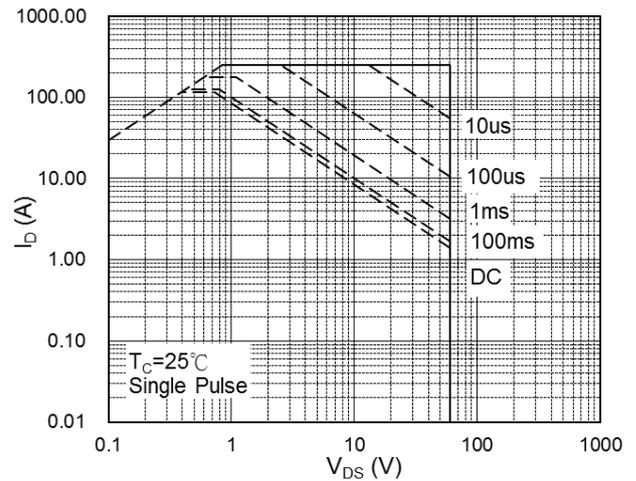


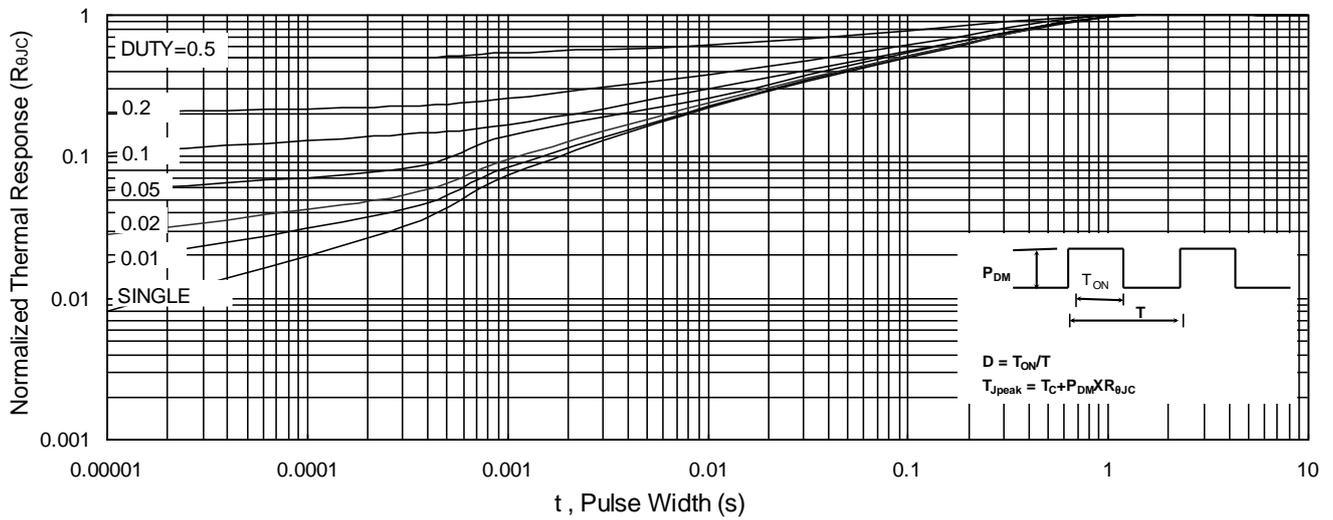
Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$



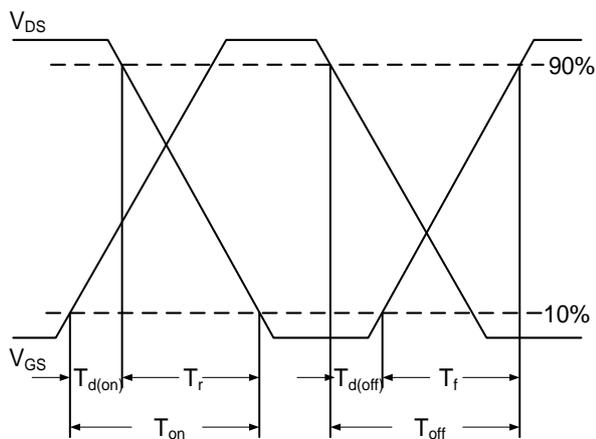
**Fig.7 Capacitance**



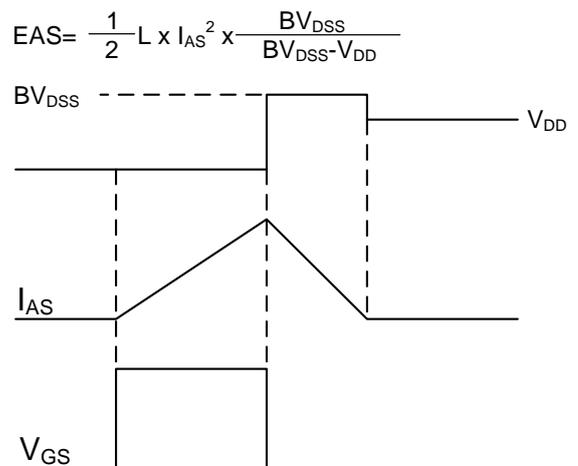
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



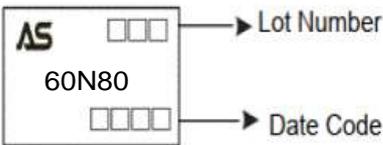
**Fig.10 Switching Time Waveform**



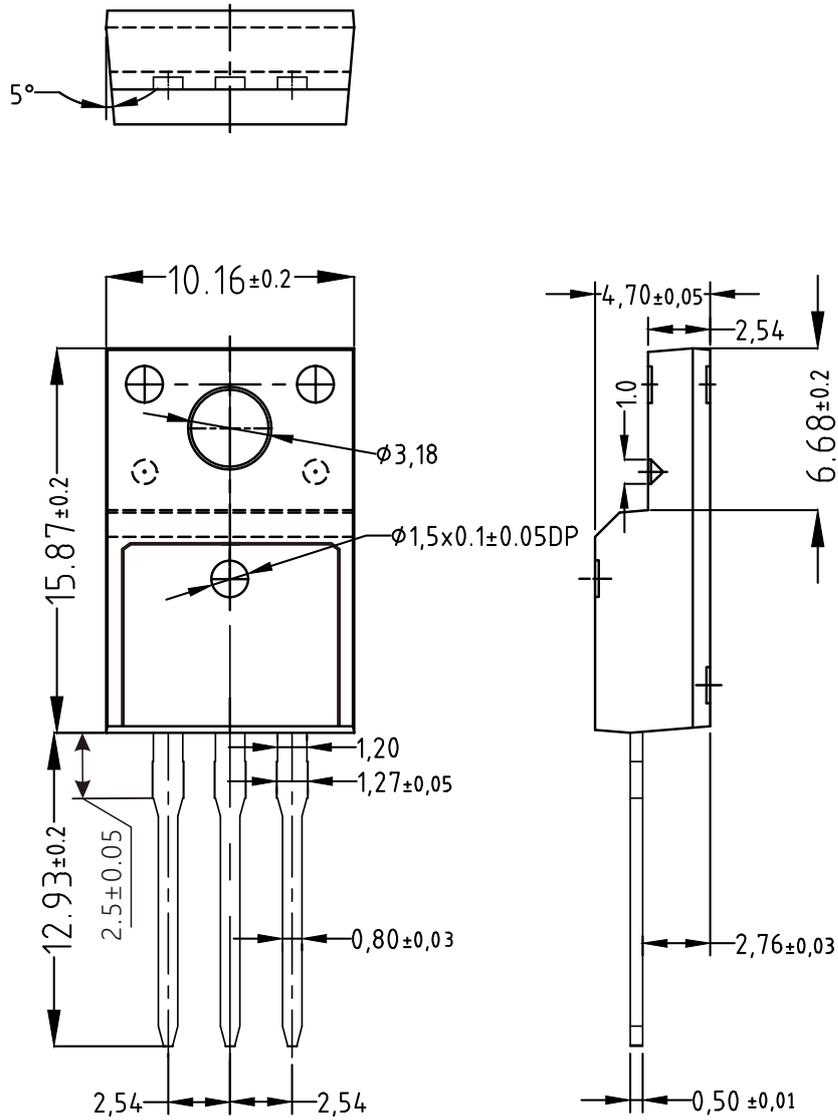
**Fig.11 Unclamped Inductive Switching Waveform**

### Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM60N80F-T	60N80	TO-220F	Tube	50/Tube

PACKAGE	MARKING
TO-220F	 <p>The diagram shows a rectangular marking area on a TO-220F MOSFET. It contains the following information:                 <ul style="list-style-type: none"> <li>Top left: <b>AS</b> logo</li> <li>Top right: Three empty boxes (□□□) with an arrow pointing to the text <b>Lot Number</b>.</li> <li>Middle: <b>60N80</b></li> <li>Bottom right: Four empty boxes (□□□□) with an arrow pointing to the text <b>Date Code</b>.</li> </ul> </p>

# TO-220F



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