Features

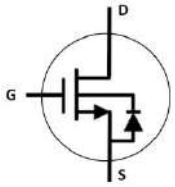
- Easy to use, compatible with standard gate drivers
- Low Q_{rr}, no free-wheeling diode required
- Excellent Q_g x $R_{DS(on)}$ product (FOM)
- Low switching loss
- RoHS compliant and Halogen-free

Product Summary

V_{DS}	650	V
$R_{DS(on),Max}$ @ V_{GS}= 8V	270	mΩ
I_D	13	A

Application

- Power adapters
- Telecom and datacom
- Automotive
- Servo motors



Maximum ratings, at T_C=25 °C, unless otherwise specified

Symbol	Parameter		Limit Value	Unit
I_D	Continuous drain current @T_C=25°C		13	A
	Continuous drain current @T_C=100°C		8	A
I_{DM}	Pulsed drain current @T_C=25°C (pulse width: 10us)		52	A
	Pulsed drain current @T_C=150°C (pulse width: 10us)		28	A
V_{DSS}	Drain to source voltage (T_J = -55°C to 150°C)		650	V
V_{GSS}	Gate to source voltage		±20	V
P_D	Maximum power dissipation @T_C=25°C		70	W
T_C	Operating temperature	Case	-55 to 150	°C
T_J		Junction	-55 to 150	°C
T_S	Storage temperature		-55 to 150	°C
T_{CSOLD}	Soldering peak temperature		260	°C

Thermal Resistance

Symbol	Parameter	Typical	Unit
$R_{\Theta JC}$	Junction-to-case	1.8	°C/W
$R_{\Theta JA}$	Junction-to-ambient	50	°C/W

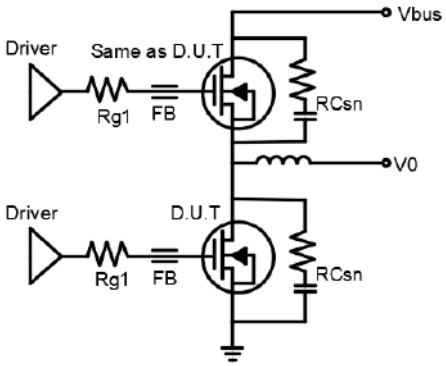
Electrical Parameters, at T_J=25 °C, unless otherwise specified

Symbol	Min	Typ	Max	Unit	Test Conditions
Forward Device Characteristics					
$V_{DSS-MAX}$	650	-	-	V	V_{GS}=0V
BV_{DSS}	-	1500	-	V	V_{GS}=0V, I_{DSS}=250μA
$V_{GS(th)}$	-	1.82	-	V	V_{DS}=V_{GS}, I_D=500μA
$R_{DS(on)}$[a]	-	-	270	mΩ	V_{GS}=8V, I_D=4A, T_J=25°C
	-	450	-		V_{GS}=8V, I_D=4A, T_J=150°C
I_{DSS}	-	8	20	μA	V_{DS}=700V, V_{GS}=0V, T_J=25°C
	-	25	-	μA	V_{DS}=700V, V_{GS}=0V, T_J=150°C
I_{GSS}	-	-	150	nA	V_{GS}=20V
	-	-	-150	nA	V_{GS}=-20V
C_{ISS}	-	490	-	pF	V_{GS}=0V, V_{DS}=650V, f=1MHz
C_{OSS}	-	25	-	pF	
C_{RSS}	-	4	-	pF	
$C_{O(er)}$	-	30	-	pF	V_{GS}=0V, V_{DS}=0 to 650V
$C_{O(tr)}$	-	50	-	pF	
Q_G	-	12.5	-	nC	V_{DS}=400V, V_{GS}=0V to 8V, I_D=10A
Q_{GS}	-	3	-		
Q_{GD}	-	2.8	-		
$t_{D(on)}$	-	16	-	nS	V_{DS}=400V, V_{GS}=0V to 12V, I_D=10A, R_G=21Ω
t_R	-	14	-		
$t_{D(off)}$	-	70	-		
t_F	-	7	-		
Reverse Device Characteristics					
V_{SD}	-	1.7	-	V	V_{GS}=0V, I_S=5A, T_J=25°C
	-	2.6	-		V_{GS}=0V, I_S=10A, T_J=25°C
	-	5	-		V_{GS}=0V, I_S=10A, T_J=150°C
t_{RR}	-	18	-	ns	I_S=10A, V_{GS}=0V, d_i/d_t=1200A/us, V_{DD}=400V
Q_{RR}	-	38	-	nC	

Notes:

a. Dynamic on-resistance

Circuit Implementation



Recommended Drive Circuit

Recommended gate drive: (0 V, 12 V) with $R_{G(tot)}$ =21 Ω, where $R_{G(tot)}$ = Rg1 + R_{Driver}

Gate Ferrite Bead (FB)	Gate Resistance1 (Rg1)	RC Snubber (RCsn)
300 Ω@100MHz	20 Ω	47 pF + 15 Ω

Notes:

a. RCsn should be placed as close as possible to the drain pin

b. The layout and wiring of the drive circuit should be as short as possible

Typical Characteristics, at T_C=25 °C, unless otherwise specified

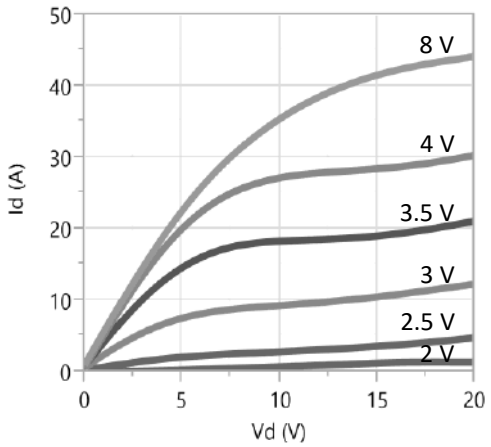


Figure 1. Typical Output Characteristics T_J=25°C

Parameter: V_{GS}

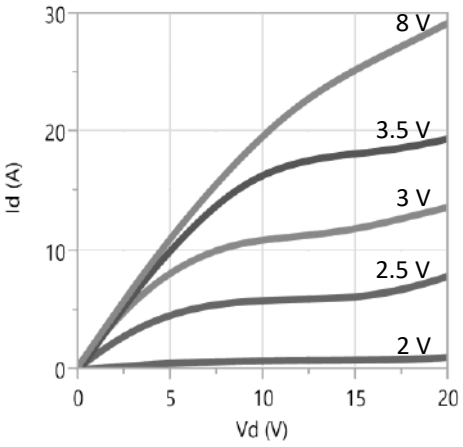


Figure 2. Typical Output Characteristics T_J=150°C

Parameter: V_{GS}

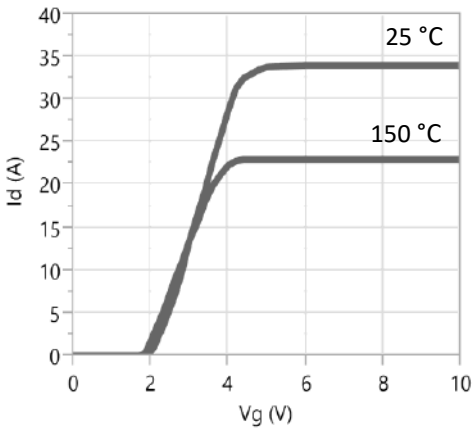


Figure 3. Typical Transfer Characteristics

V_{DS}=10V, Parameter: T_J

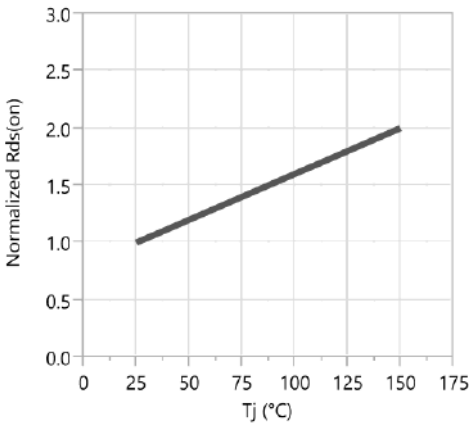


Figure 4. Normalized On-resistance

I_D=4A, V_{GS}=8V

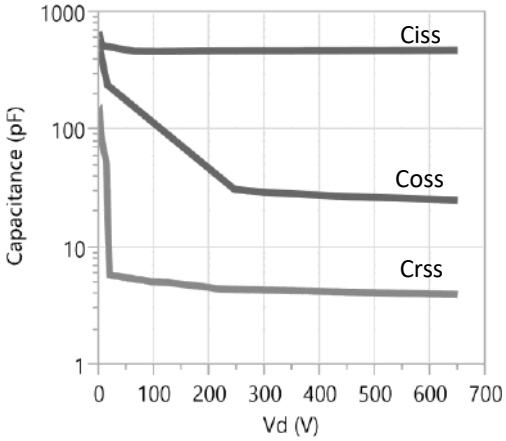


Figure 5. Typical Capacitance

V_{GS}=0V, f=1MHZ

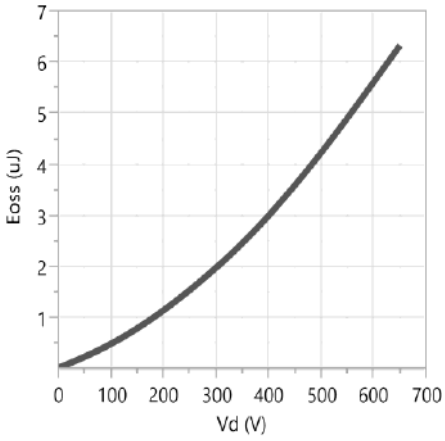


Figure 6. Typical Coss Stored Energy

Typical Characteristics, at T_C=25 °C, unless otherwise specified

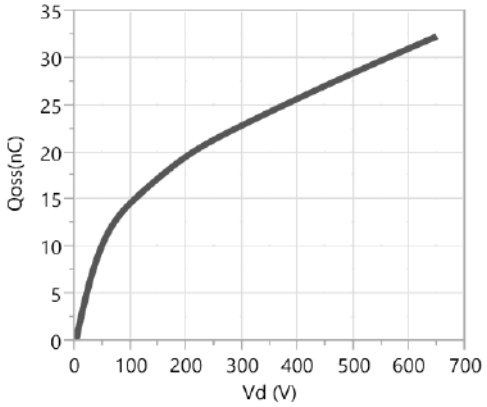


Figure 7. Typical Qoss

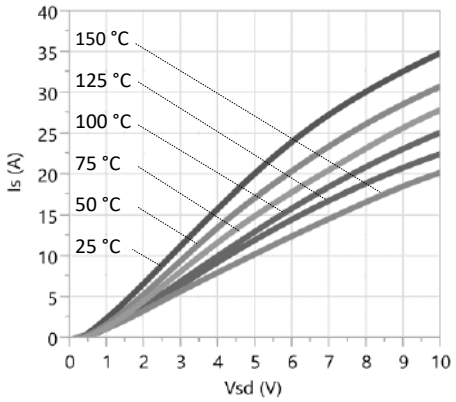


Figure 8. Forward Characteristic of Rev. Diode

$I_S=f(V_S)$, Parameter T_J

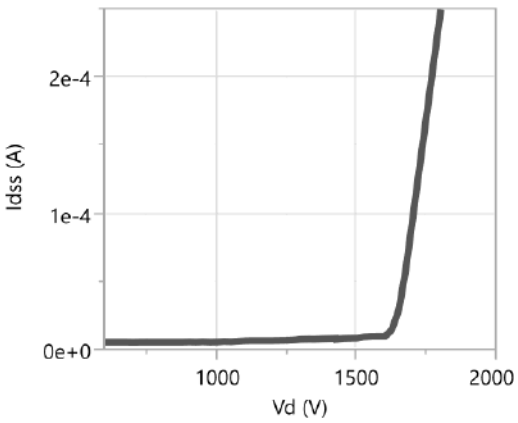


Figure 9. Drain-Source Breakdown Voltage

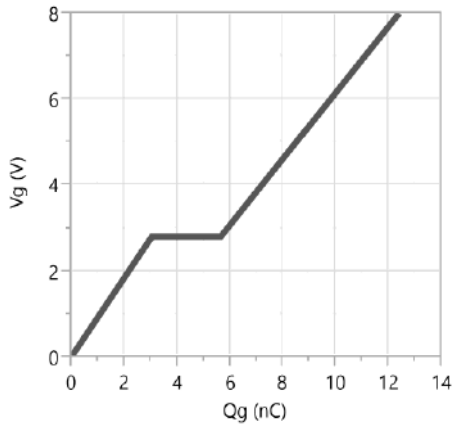


Figure 10. Typical Gate Charge

I_{DS}=10A, V_{DS}=400V

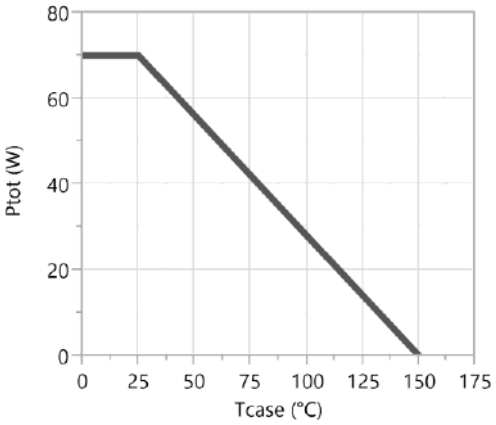


Figure 11. Power Dissipation

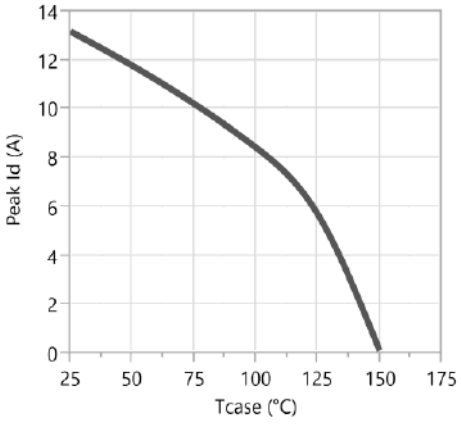


Figure 12. Current Derating

Test Circuits and Waveforms

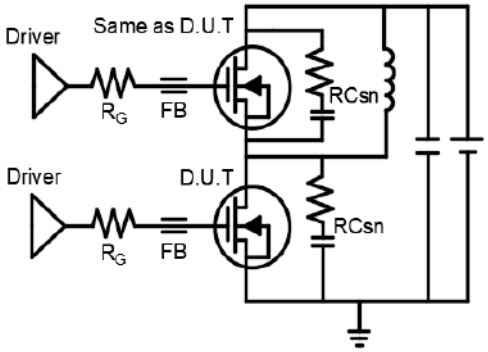


Figure 13. Switching Time Test Circuit

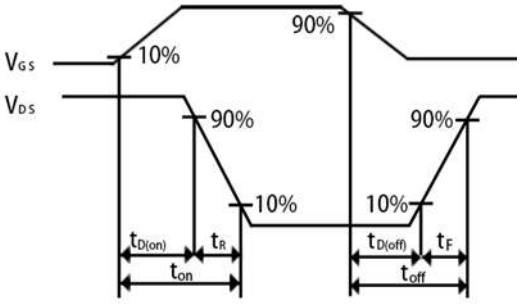


Figure 14. Switching Time Waveform

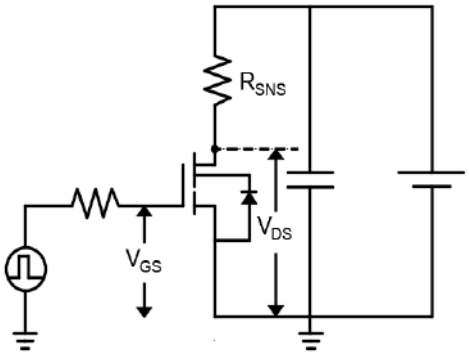


Figure 15. Dynamic $R_{DS(on)eff}$ Test Circuit

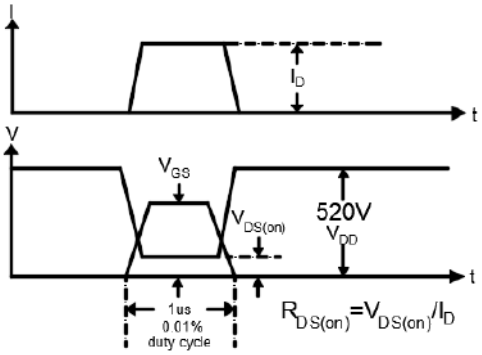


Figure 16. Dynamic $R_{DS(on)eff}$ Waveform

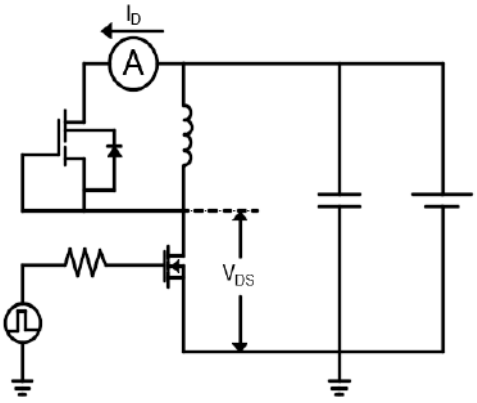


Figure 17. Diode Characteristic Test Circuits

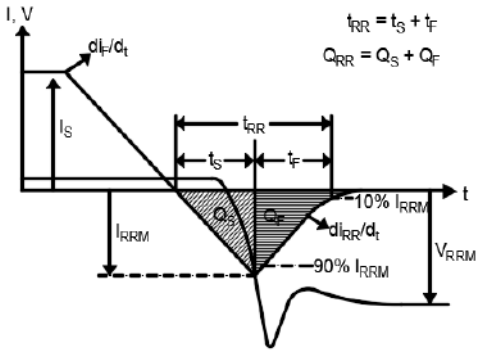


Figure 18. Diode Recovery Waveform

Design Considerations

Fast switching GaN device can reduce power conversion losses, and thus enable high frequency operations. Certain PCB design rules and instructions, however, need to be followed to take full advantages of fast switching GaN devices.

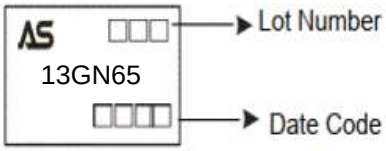
Before evaluating Ascend's GaN devices, please refer to the table below which provides some practical rules that should be followed during the evaluation.

When Evaluating Ascend's GaN Devices:

DO	DO NOT
Make sure the traces are as short as possible for both drive and power loops to minimize parasitic inductance	Using Ascend's devices in GDS board layouts
Use the test tool with the shortest inductive loop, and make sure test points should be placed close enough	Use differential mode probe or probe ground clip with long wires
Minimize the lead length of TO packages when installing them to PCB	Use long traces in drive circuit, or long lead length of the devices

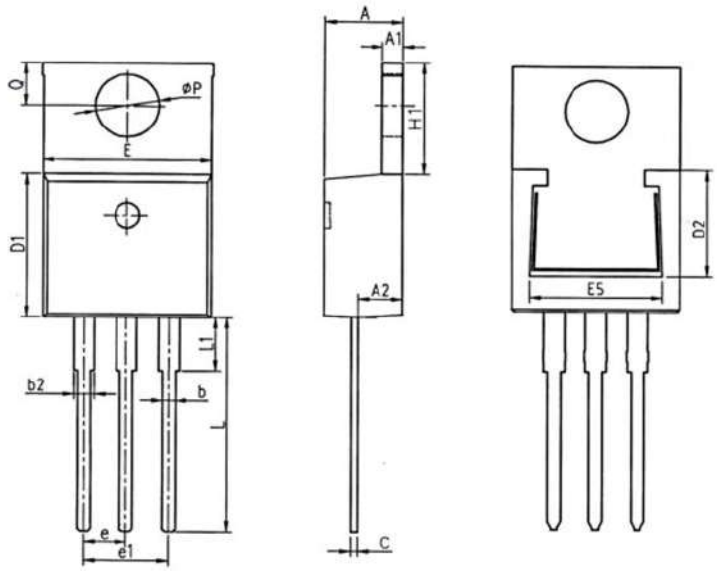
Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM13GN65P-T	13GN65	TO-220	Tube	50/Tube

PACKAGE	MARKING
TO-220	

Mechanical **3 Lead TO-220 Package**

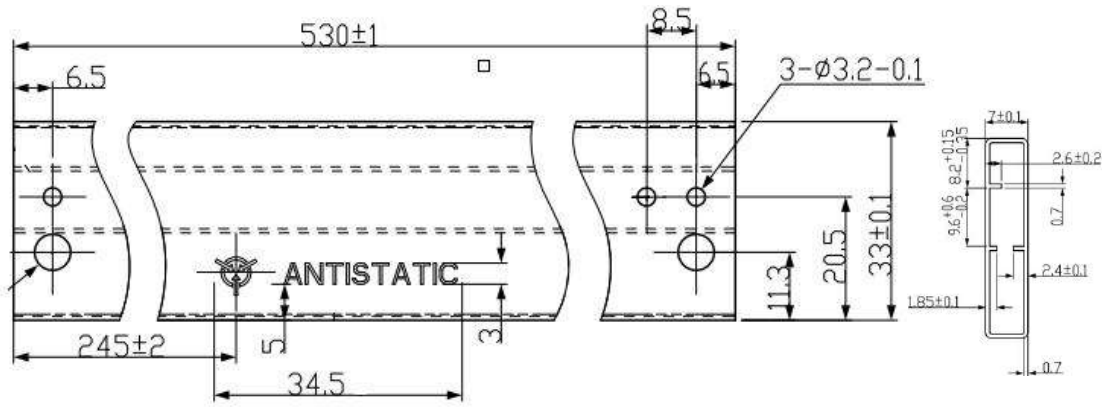
Pin 1: Gate; Pin 2: Source; Pin 3: Drain; Tab: Source



COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
b	0.75	0.81	0.96
b2	1.22	1.27	1.47
c	0.30	0.38	0.48
D1	8.50	8.70	8.90
D2	5.20	-	-
E	9.86	10.16	10.36
E5	7.06	-	-
e		2.54 BSC	
e1		5.08 BSC	
H1	6.10	6.30	6.50
L	13.10	13.40	13.70
L1	-	3.75	4.10
ΦP	3.70	3.84	3.99
Q	2.54	2.74	2.94

Package Outlines **Dimensions are show in millimeters**



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