

Features

- Low dynamic output impedance.
- Sink current capability of 1 to 100mA.
- Low output noise voltage
- Fast turn on response

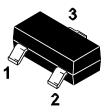
Application

• It provides very wide applications, including shunt

regulator, series regulator, switching regulator, voltage

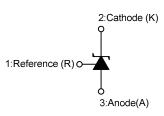
reference and others.





1. Reference 2.Cathode 3.Anode





Absolute Maximum Ratings (Ta=25°Cunless otherwise specified)

Parameter	Symbol	Value	Units
Cathode Voltage	V _{KA}	20	V
Cathode Current Range(Continuous)	I _{KA}	-100 ~ +100	mA
Reference Input Current Range	I _{REF}	10	mA
Maximum Power Dissipation	P _D	350	mW
Typical Thermal Resistance	R _{θJA}	130	°C/W
Operating Junction Temperature	TJ	150	°C
Storage Temperature Range	T _{STG}	-65 ~ +150	°C

Recommended Operating Conditions

Parameter	Symbol	Min.	Max.	Units
Cathode Voltage	V _{KA}	V _{REF}	18	V
Cathode Current	I _{KA}	0.1	100	mA
Operating Ambient Temperature Range	T _{OPR}	-40	125	°C



Electrical Characteristics (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test C	onditions	Min.	Тур.	Max.	Unit
Reference Input Voltage Fig1	VREF	V _{KA} =V _{REF} , I _{KA} =10mA	ASPL432A(1%)	1.238	1.25	1.262	V
			ASPL432AC(0.5%)	1.244	1.25	1.256	V
			0°C ≤T _A ≤70°C	-	2	10	mV
Deviation of Reference Input Voltage Over Temperature Fig1	ΔV_{REF}	V _{KA} =V _{REF} , I _{KA} =10mA	-20°C ≤T _A ≤125°C	-	3	15	mV
tokago otor tompolataro			-40°C ≤T _A ≤125°C	-	8	25	mV
Ratio of Change in Reference Input Voltage to The Change in Cathode Voltage Fig2	<u>ΔVref</u> ΔVka	I _{KA} =10mA, ∆V _{KA} =V _{REF} ~16V		-	-0.5	-1.5	mV/V
Reference Input Current Fig2	REF	I _{KA} =10mA, R1=10KΩ, R2=∞		-	0.15	0.4	μA
Deviation of Reference Input Current Over Full Temperature Range Fig2	ΔIREF	I _{KA} =10mA, R1=10KΩ, R2=∞, -20°C ≤T _A ≤+85°C		-		0.4	μA
Minimum Cathode Current for Regulation Fig1	Ika(min)	VKA=VREF		-		80	μA
Off-State Cathode Current Fig3	KA(OFF)	VKA=18V, VREF=0		-	0.04	0.5	μA
Dynamic Impedance	Zka	V _{KA} =V _{REF} , I _{KA} =1~ 100mA, f≤1.0KHz			0.05	0.15	Ω

Figure 1. Test Circuit for $V_{KA} = V_{REF}$

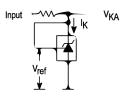
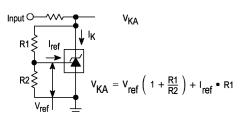


Figure 2. Test Circuit for $V_{KA} > V_{REF}$

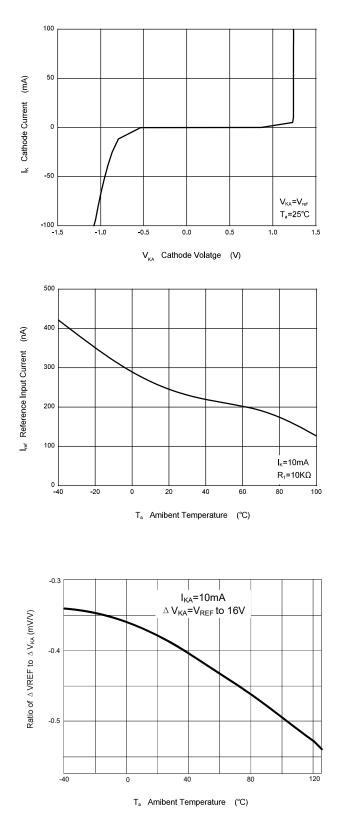
Figure 3. Test Circuit for $I_{\mbox{\scriptsize OFF}}$

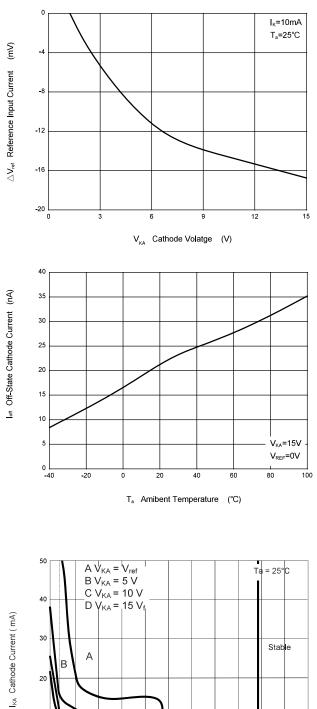


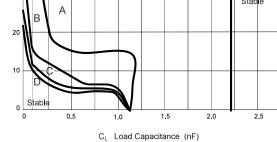
Input O-W Ioff



Typical Characteristic Curves









Ordering Information

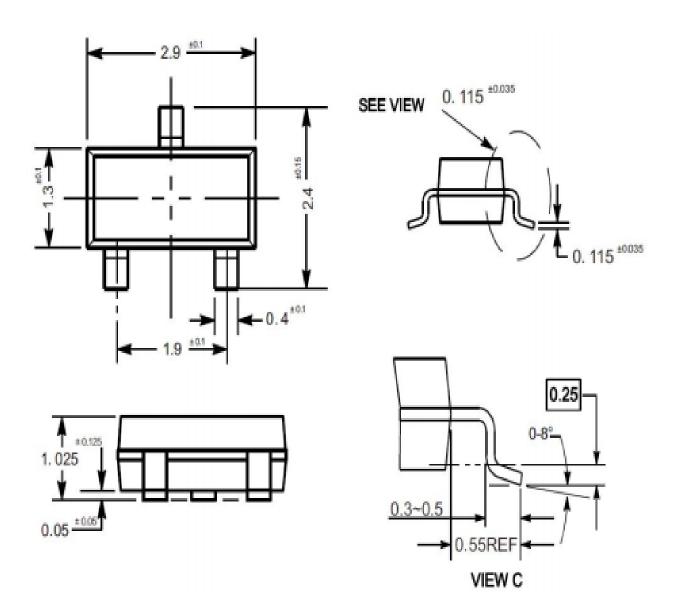
Ordering Number		Deekege	Packing	Quantity	
Halogen Free	Lead Free	Package	Facking	Quantity	
ASPL432AZA-R		SOT23	Tape Reel	3000	

PACKAGE	MARKING
SOT23	正面丝印



Package Outline

SOT23 Dimensions in mm





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